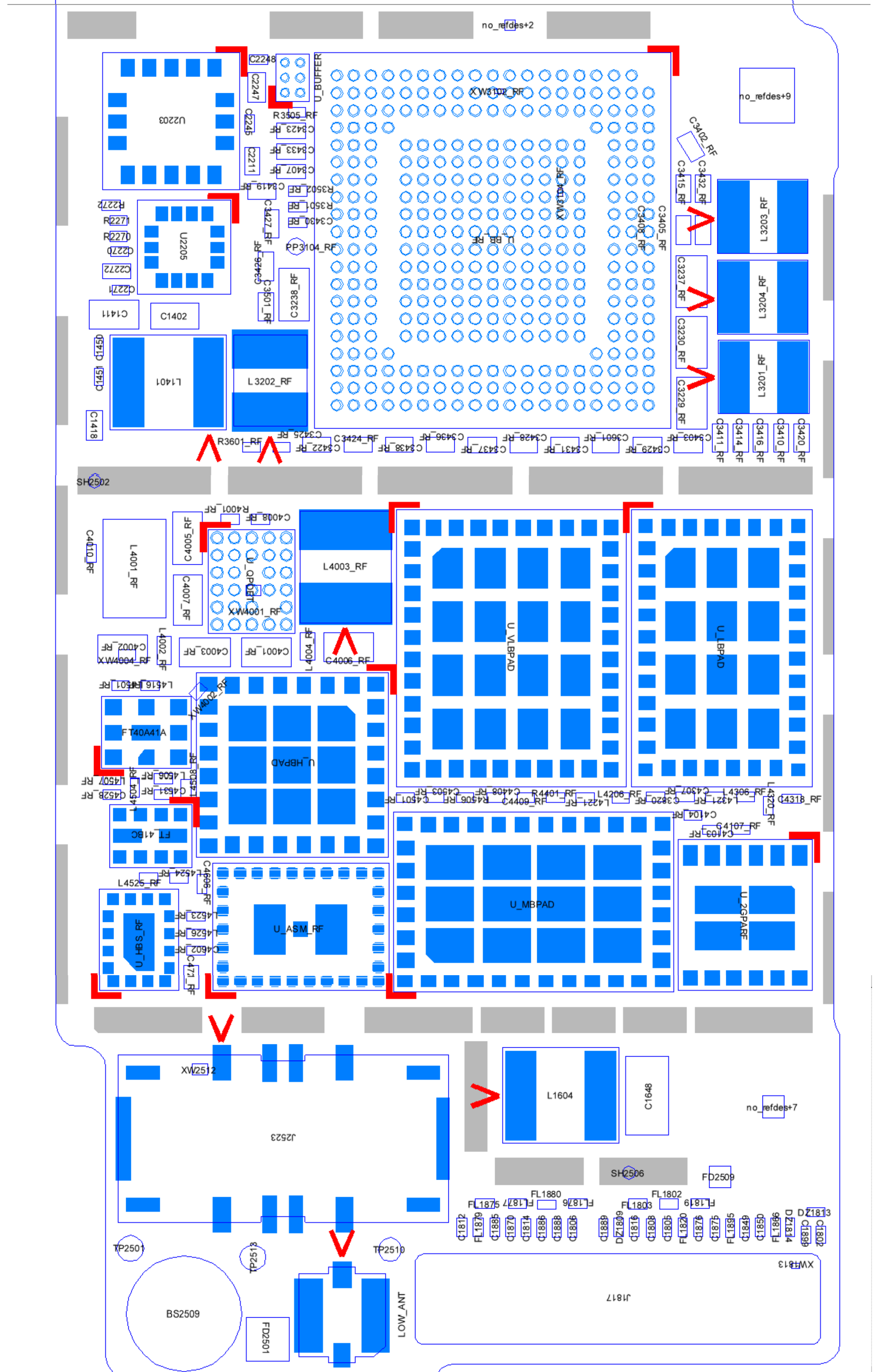
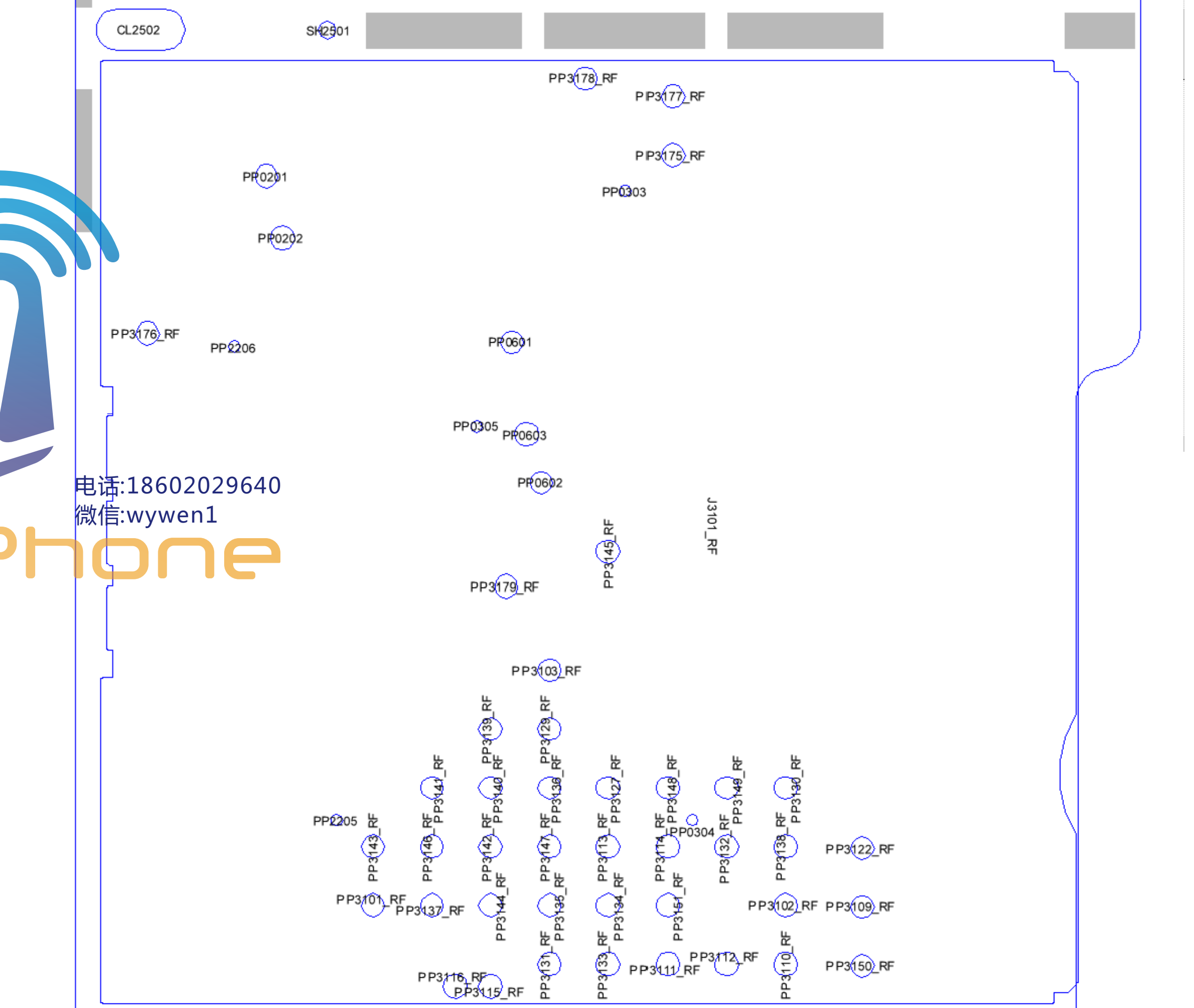
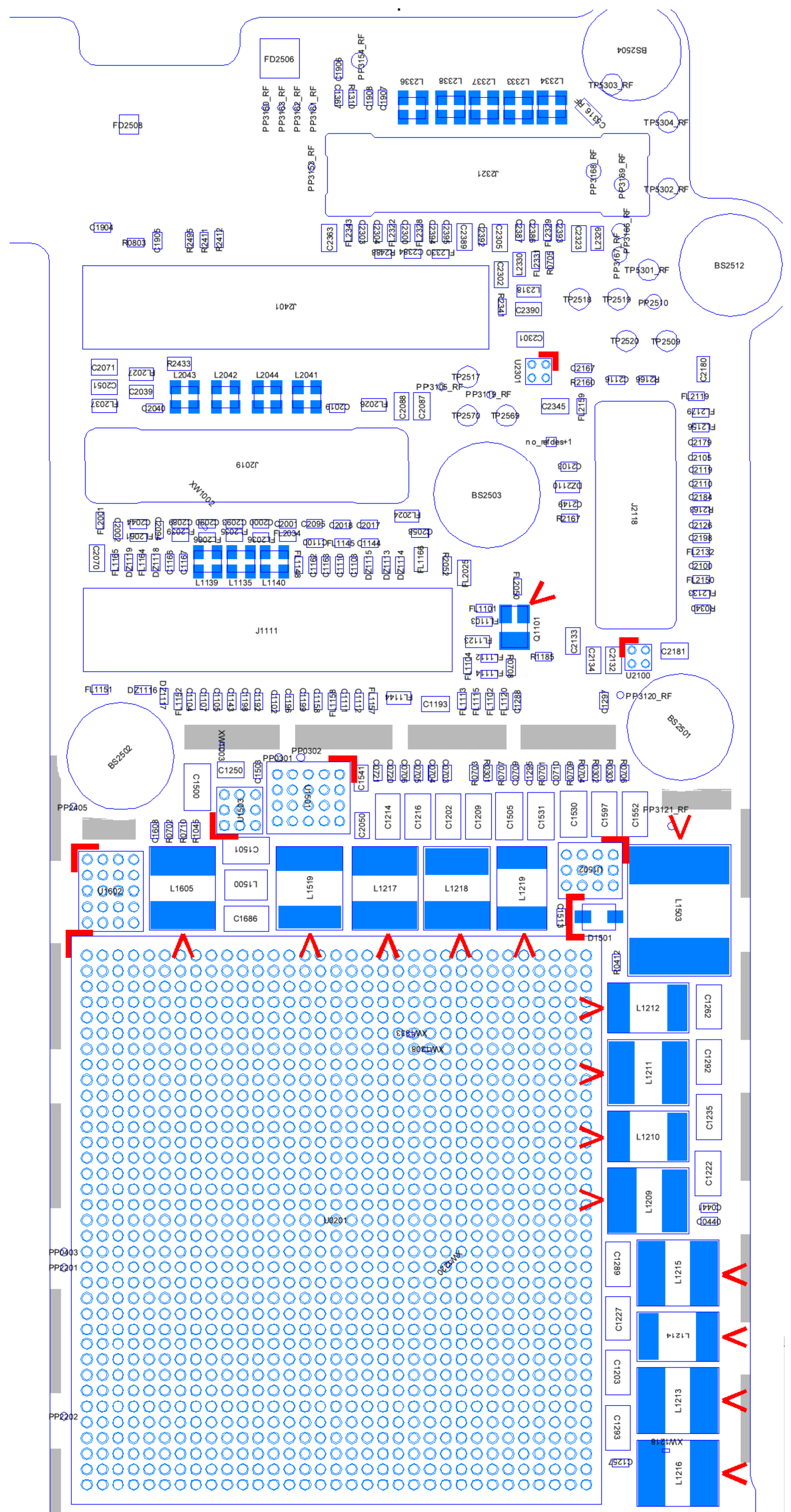
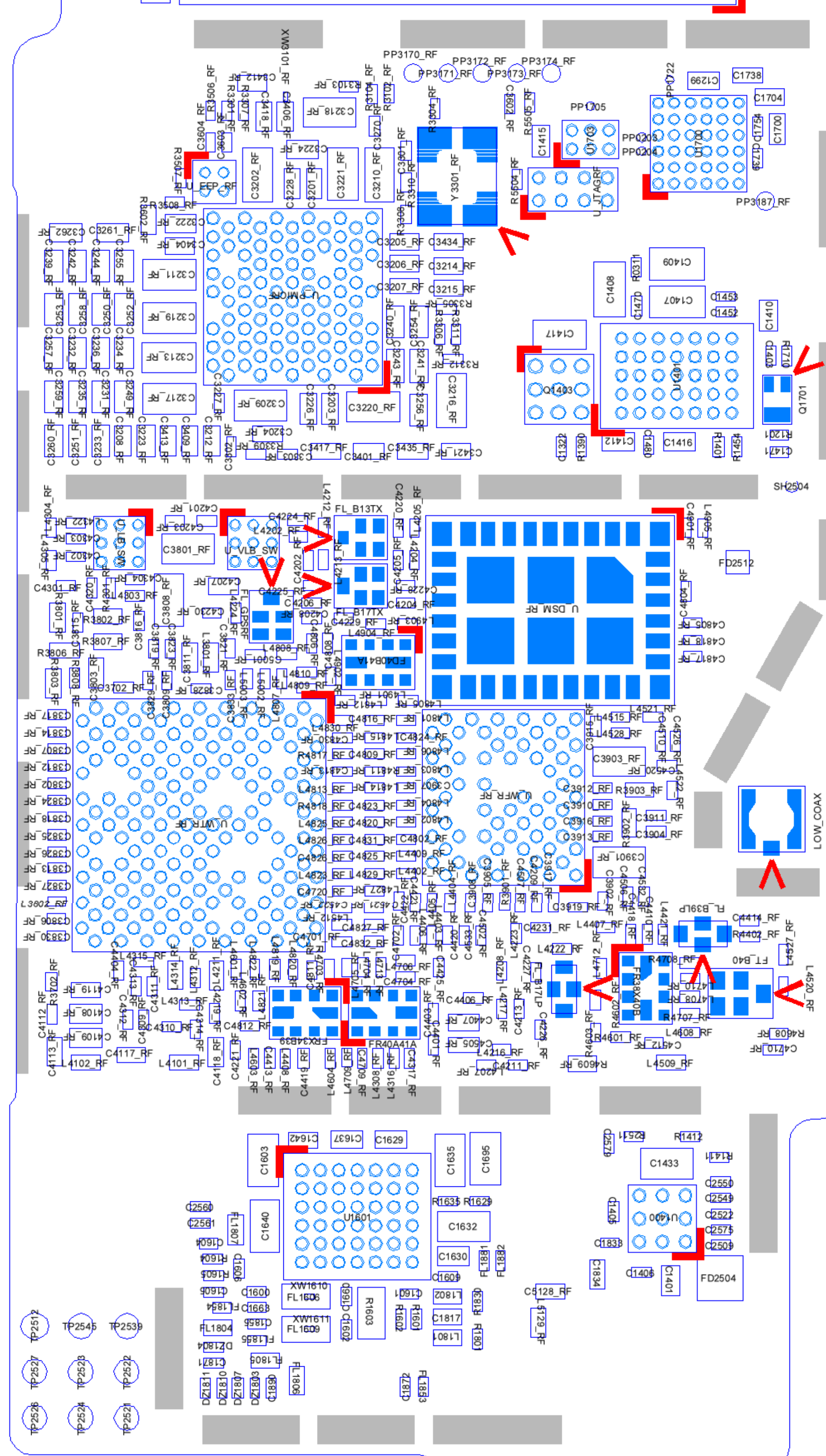
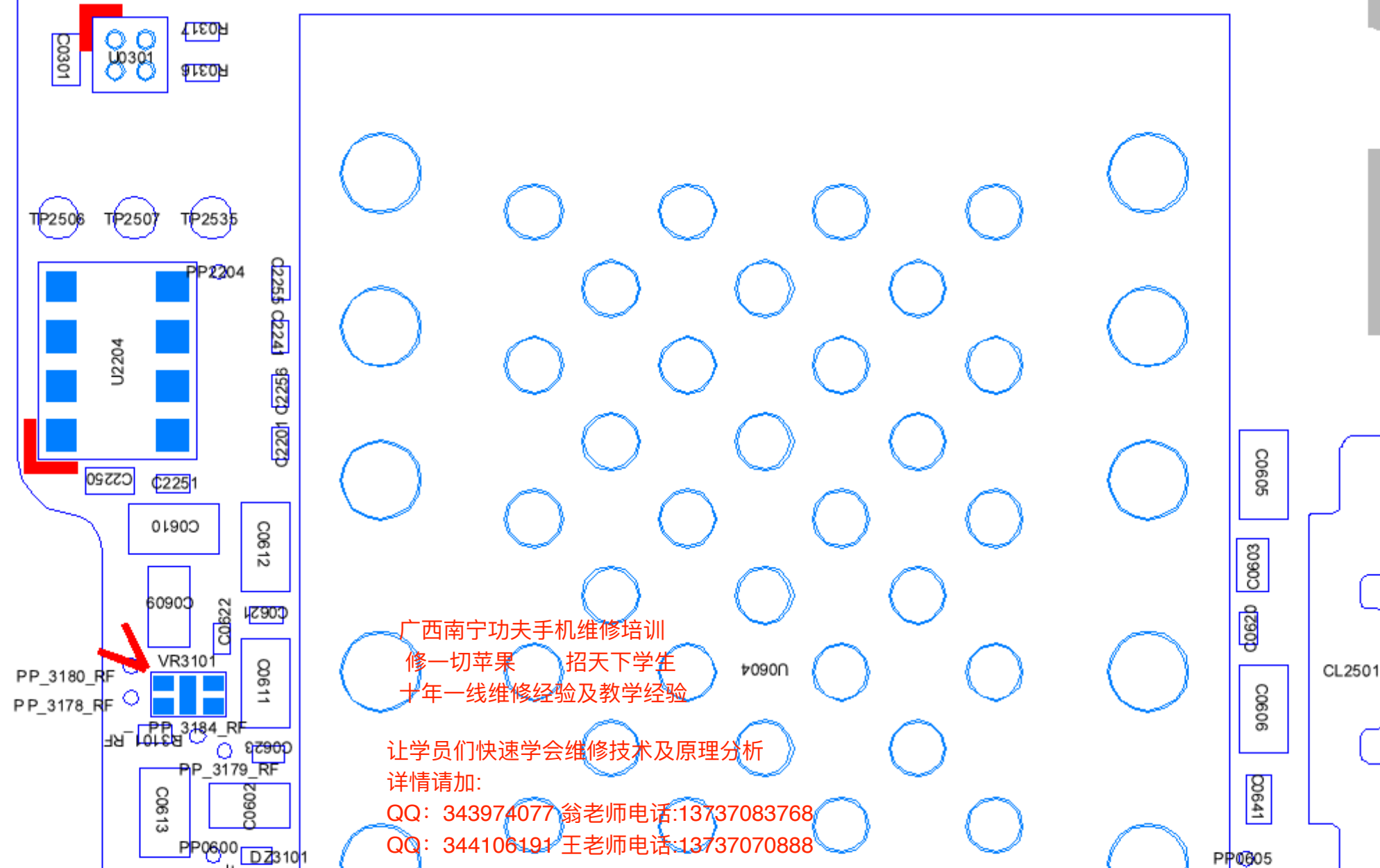
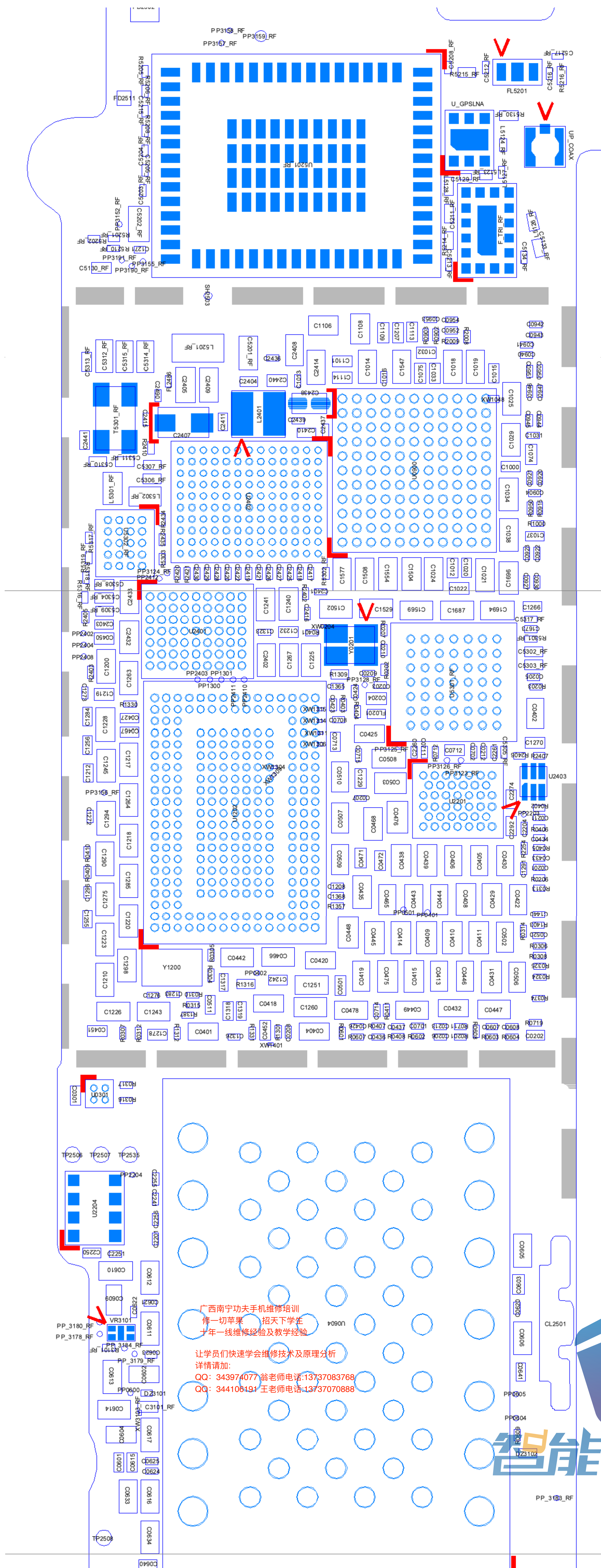


iphone6 4.7



Thu Apr 17 17:11:44 2014

N61 BOM CALLOUTS

NAND BOM OPTIONS

ALTERNATE NAND BOM OPTIONS

SHIELD BOM OPTIONS

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ALTERNATE BOM OPTIONS

SCH 051-9903
BRD 820-3486
MCO 056-6825

BOM 639-00208 (16GB,BETTER,DTD)

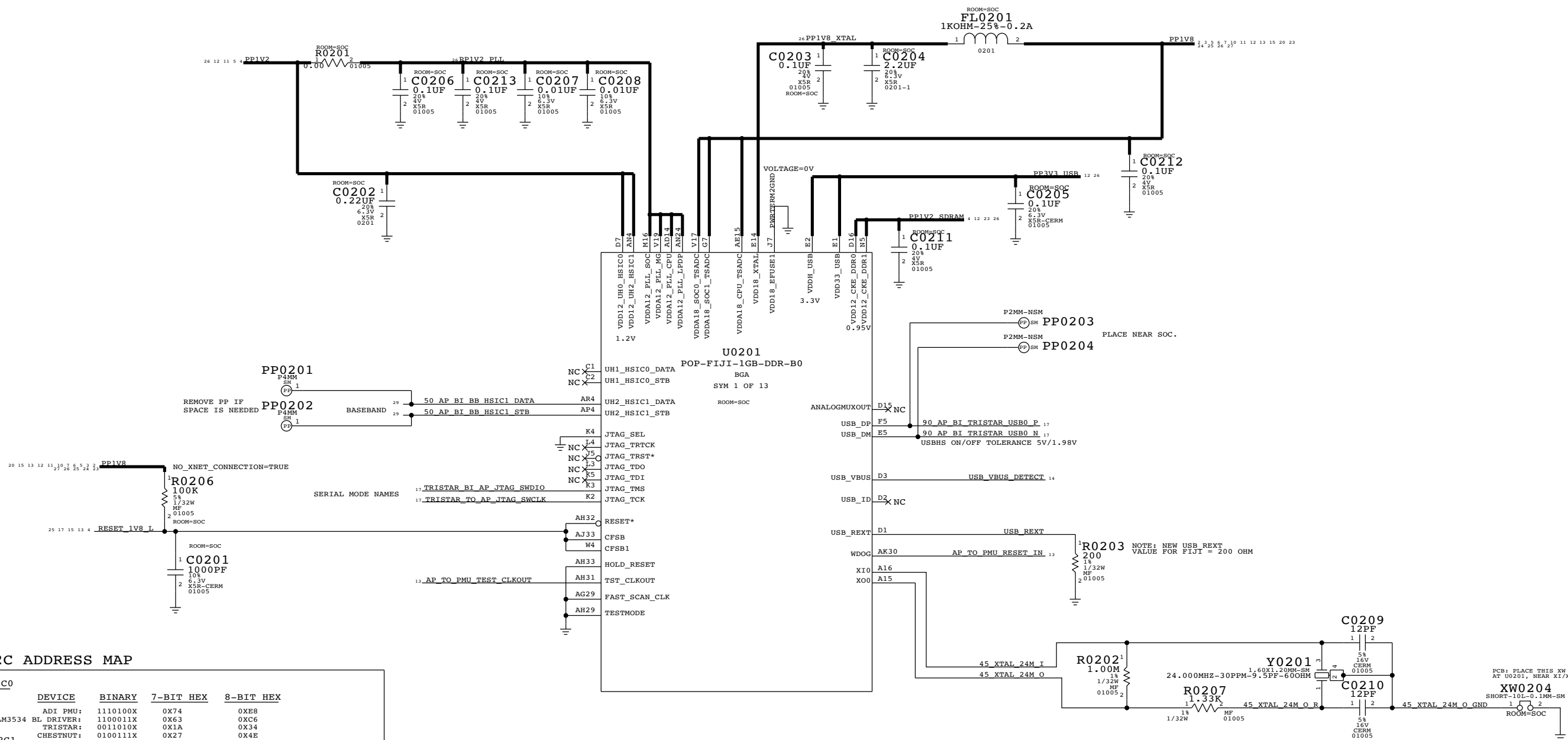
BOM 639-00209 (32GB,BEST,DTD)

BOM 639-00210 (64GB, ULTRA, DTD)

BOM 639-00025 (128GB, SUPREME, TLC) BOM 639-00212 (128GB, SUPREME, TLC, DTD)

DRAWING TITLE		SCHEM, MLB, N61	
 Apple Inc.		DRAWING NUMBER	051-9903
		SIZE	D
		REVISION	7.0.0
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		SHEET	1 OF 54

FIJI: JTAG, USB, HSIC, XTAL



I2C ADDRESS MAP

I2C0	DEVICE	BINARY	7-BIT HEX	8-BIT HEX
LM3534	ADI PMU:	1110100X	0X74	0XE8
	BL DRIVER:	1100011X	0X63	0XC6
	TRISTAR:	0011010X	0X1A	0X34
	CHESTNUT:	0100111X	0X27	0X4E
I2C1	TIGRIS CHARGER:	1110101X	0X75	0XE9
	LINEAR VIBE:	1011010X	0X5A	0XB4
	CS35L19B AMP:	1000000X	0X40	0X80
	MESA EEPROM (MEMORY):	1010110X	0X56	0XAC
I2C2	MESA EEPROM (ID):	1011110X	0X5E	0XBC
	CT814 ALS:	0101001X	0X29	0X52
RCAM I2C	DISPLAY EEPROM:	1010001X	0X51	0XA2
	OPEL STROBE DRIVER:	1100011X	0X63	0XC6
FCAM I2C	REAR FACING CAM:	0010000X	0X10	0X20
	VCM AF DRIVER:	0001100X	0X0C	0X18

NOTE: ACCEL, GYRO, COMPASS ALL USING SPI (VIA OSCAR) FOR AP COMMUNICATION.

SYNC MASTER=N56 MLB		SYNC DATE=08/29/2013	
PAGE TITLE			
SOC:MAIN			
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		REVISION	7.0.0
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FIJI: DIGITAL I/O, BOOTSTRAPPING

D

C

B

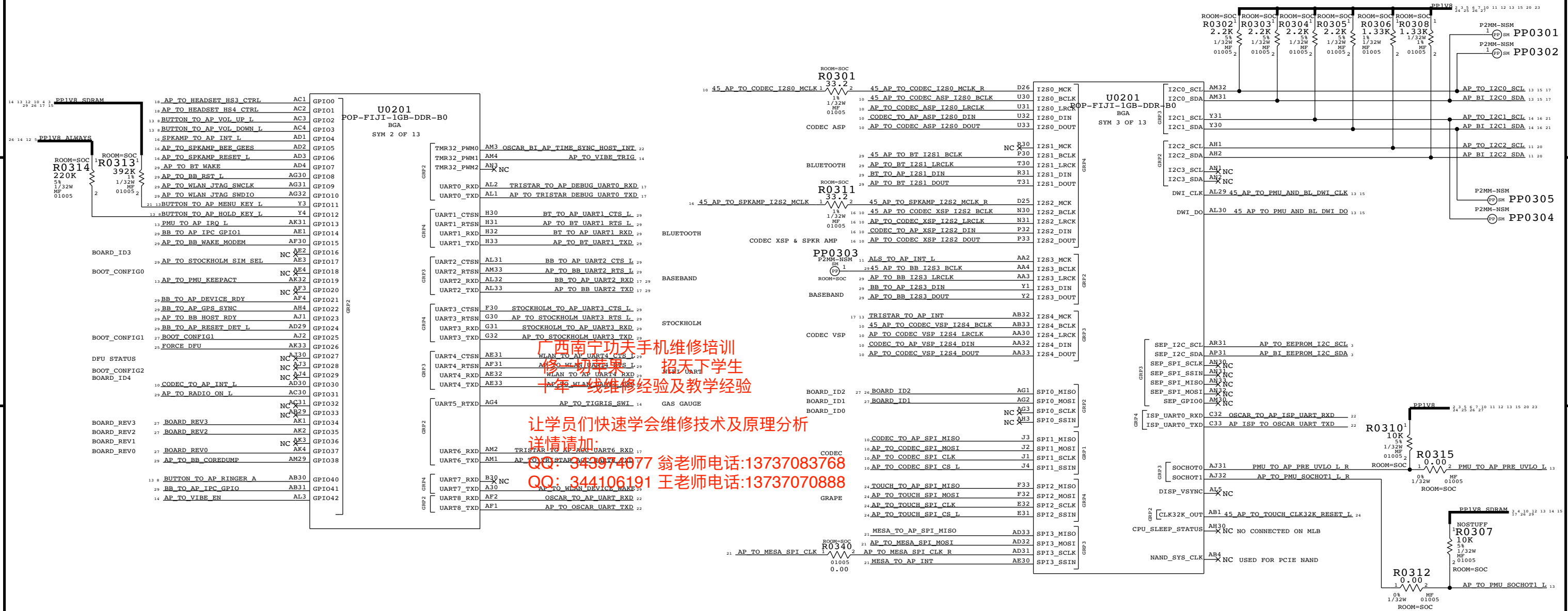
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D

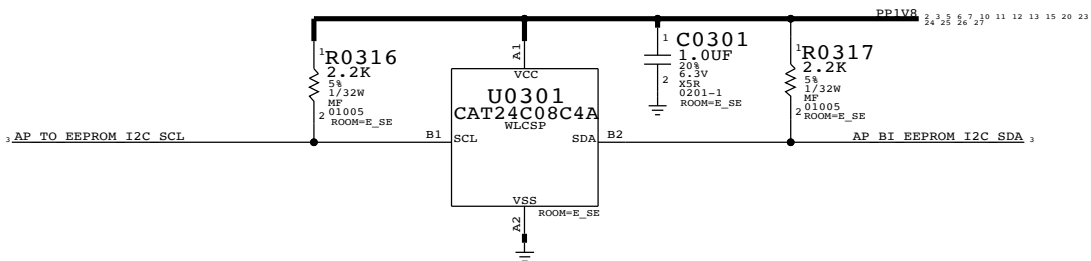
C

B

A



ANTI-ROLLBACK EEPROM
ONSEMI EEPROM
APN: 335S0894



REMOVED HOLD + MENU KEY
BUFFERS SINCE NOT NEEDED FOR FIJI

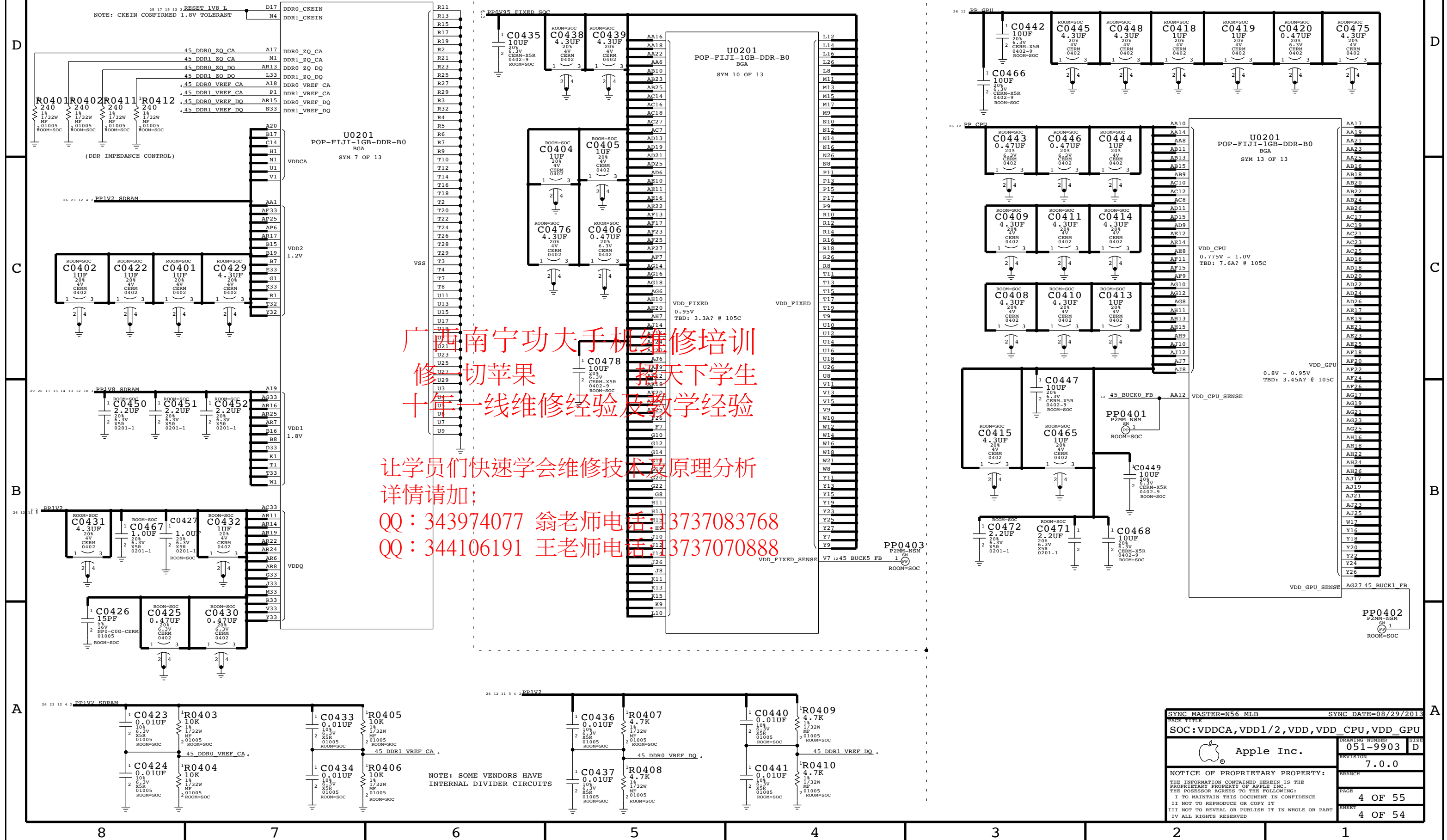
SYNC MASTER=N56 MLB		SYNC DATE=08/29/2013	
PAGE TITLE			
SOC: I / OS			
		DRAWING NUMBER	D
Apple Inc.		051-9903	
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		7.0.0	
		BRANCH	
		PAGE	3 OF 55
		SHEET	3 OF 54

FIJI: VDDCA, VDD1/2, VDDQ, VDD, VDD_FIXED, VDD_CPU, VDD_GPU

VDDCA, VDD1/2, VDDQ

VDD

VDD_CPU, VDD_GPU



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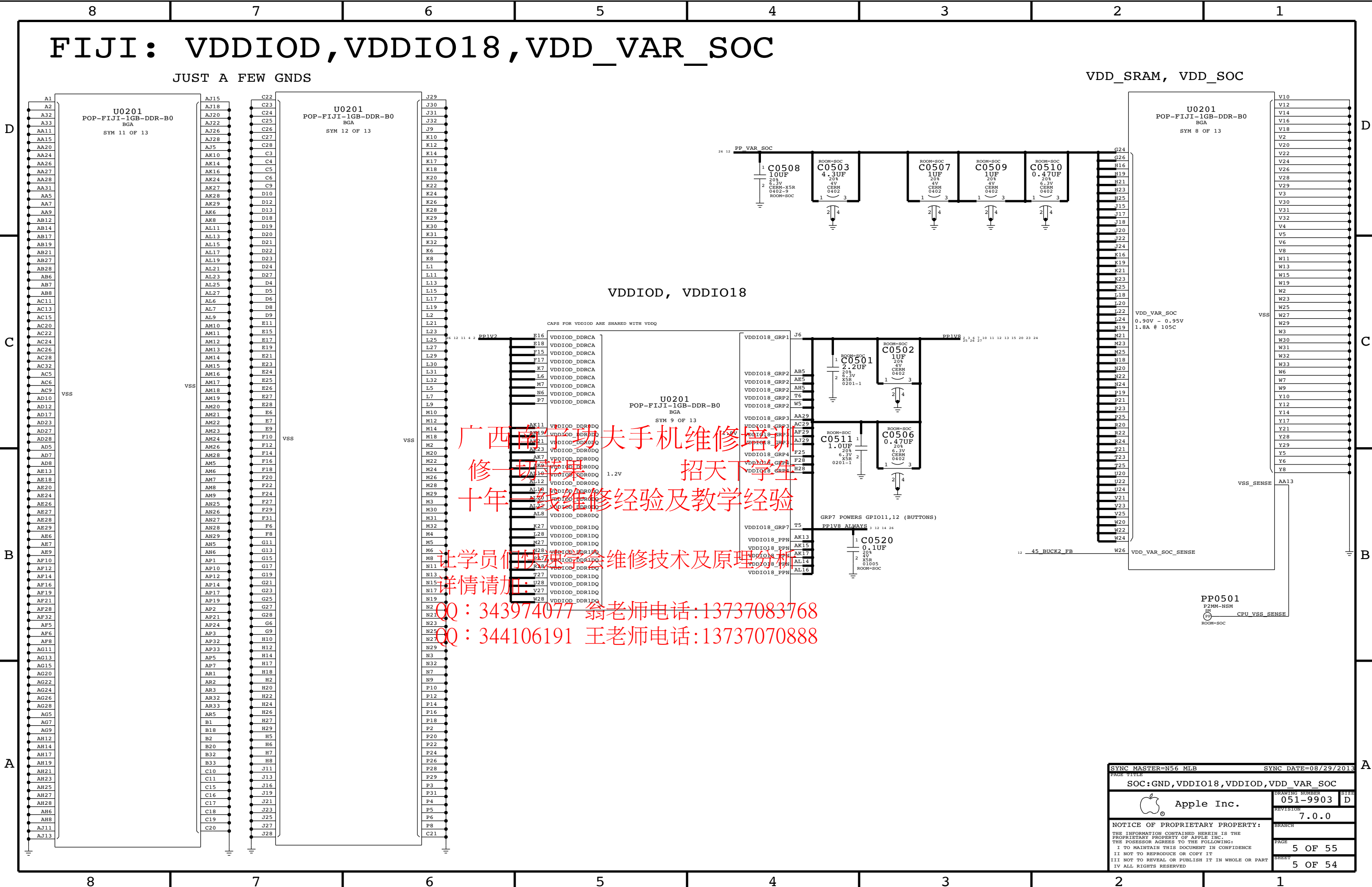
SYNC MASTER=N56 MLB		SYNC DATE=08/29/2013	
PAGE=1/1			
SOC=VDDCA,VDD1/2,VDD,VDD_CPU,VDD_GPU			
	DRAWING NUMBER		SIZE
	051-9903		D
Apple Inc.		REVISION	
		7.0.0	
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		4 OF 55	
		SHEET	
		4 OF 54	

The diagram illustrates the PCB layout for the FIJI processor, showing various components and their connections. Key components include:

- U0201**: POP-FIJI-1GB-DDR-B0 BGA, SYM 11 OF 13
- U0202**: POP-FIJI-1GB-DDR-B0 BGA, SYM 12 OF 13
- U0203**: POP-FIJI-1GB-DDR-B0 BGA, SYM 9 OF 13
- U0204**: POP-FIJI-1GB-DDR-B0 BGA, SYM 8 OF 13
- U0205**: POP-FIJI-1GB-DDR-B0 BGA, SYM 7 OF 13
- U0206**: POP-FIJI-1GB-DDR-B0 BGA, SYM 6 OF 13
- U0207**: POP-FIJI-1GB-DDR-B0 BGA, SYM 5 OF 13
- U0208**: POP-FIJI-1GB-DDR-B0 BGA, SYM 4 OF 13
- U0209**: POP-FIJI-1GB-DDR-B0 BGA, SYM 3 OF 13
- U0210**: POP-FIJI-1GB-DDR-B0 BGA, SYM 2 OF 13
- U0211**: POP-FIJI-1GB-DDR-B0 BGA, SYM 1 OF 13

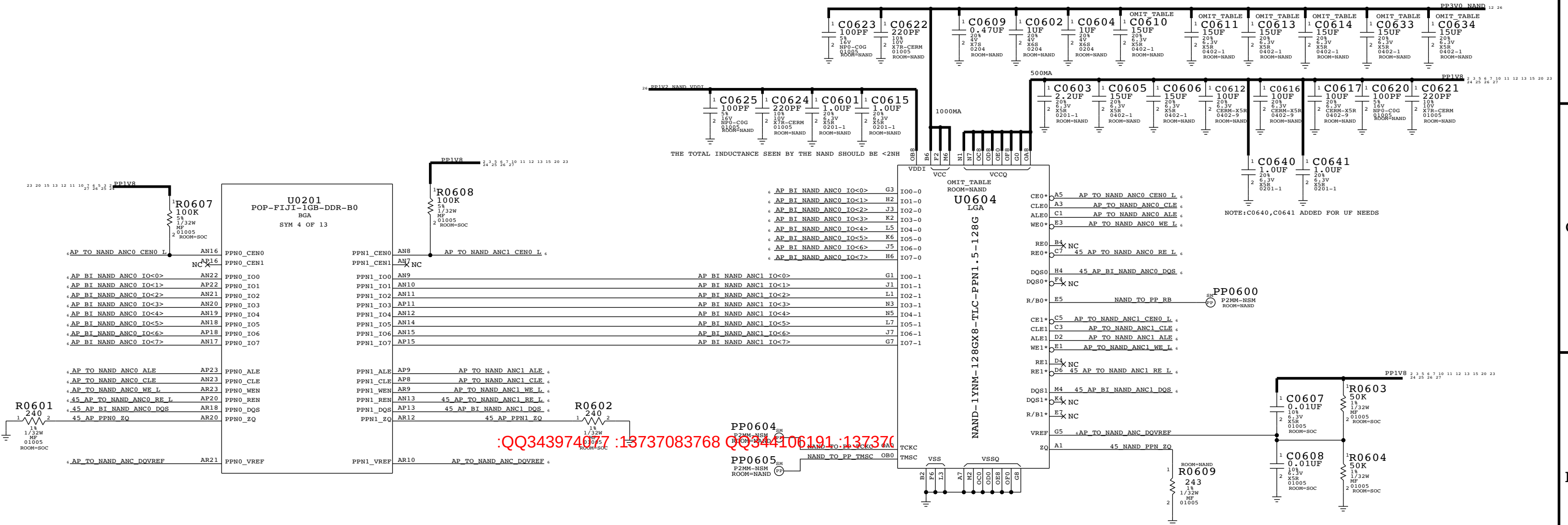
The diagram also shows various capacitors and resistors, including:

- C0501**: 100UF, 20%, 6.3V, X5R, 0402-9, ROOM-SOC
- C0502**: 4.3UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0503**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0504**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0505**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0506**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0507**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0508**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0509**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0510**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0511**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0512**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0513**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0514**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0515**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0516**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0517**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0518**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0519**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0520**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0521**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0522**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0523**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0524**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0525**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0526**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0527**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0528**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0529**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0530**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0531**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0532**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0533**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0534**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0535**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0536**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0537**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0538**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0539**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0540**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0541**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0542**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0543**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0544**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0545**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0546**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0547**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0548**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0549**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0550**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0551**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0552**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0553**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0554**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0555**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0556**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0557**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0558**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0559**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0560**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0561**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0562**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0563**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0564**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0565**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0566**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0567**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0568**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0569**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0570**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0571**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0572**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0573**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0574**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0575**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0576**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0577**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0578**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0579**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0580**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0581**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0582**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0583**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0584**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0585**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0586**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0587**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0588**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0589**: 1UF, 20%, 4V, CERM, 0402, ROOM-SOC
- C0590**: 1UF

[illegible][illegible]

FIJI: NAND + 12X17 NAND PKG

SUPPORT FOR PPN1.5 (1.8V IO) ONLY



NOTE: NAND PADS SHOULD BE SHIELDED FROM TRACES WITH A GROUND PLANE

PP0601 P4MM
ROOM=SOC 1 AP BI NAND ANCO IO<6> 45
PP0602 P4MM
ROOM=SOC 1 45 AP TO NAND ANCO RE L 45
PP0603 P4MM
ROOM=SOC 1 45 AP BI NAND ANCO DQS 45

SYNC MASTER=N56 MLB		SYNC DATE=08/29/2013	
PAGE TITLE			
SOC : NAND			
 Apple Inc.		DRAWING NUMBER	051-9903
		SIZE	D
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		BRANCH	
		PAGE	6 OF 55
		SHEET	6 OF 54

D

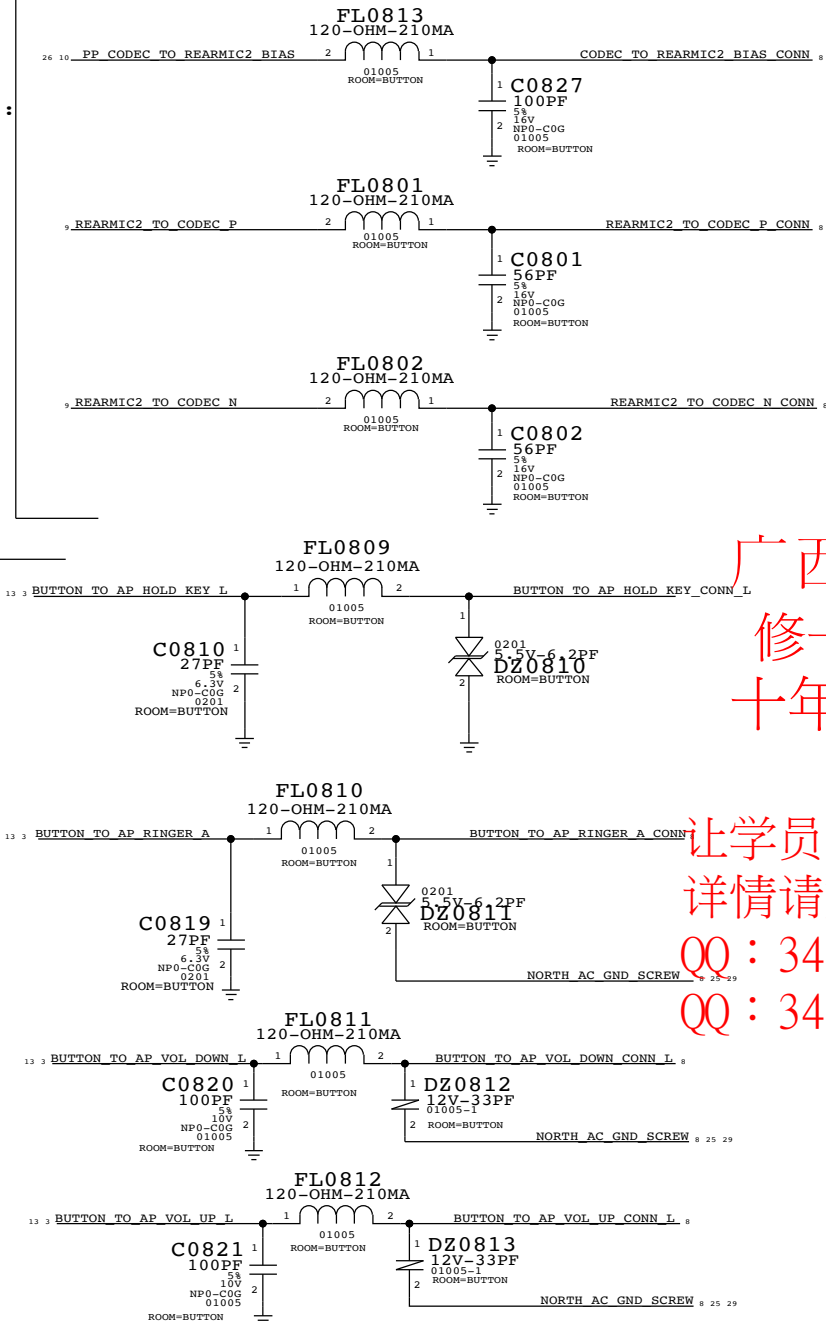
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8 7 6 5 4 3 2 1

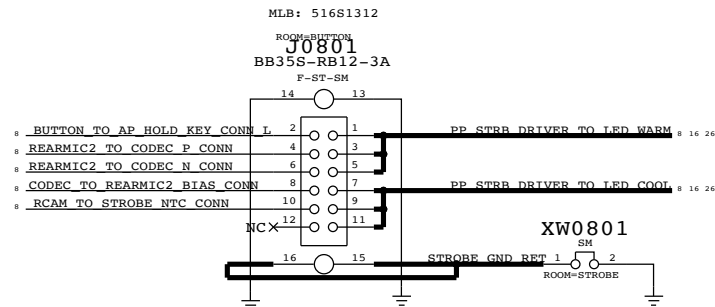
BUTTON FLEX (BUTTONS, ANC REF MIC, STROBE, STROBE_NTC, WIFI FLEX PAC)

MIC2 (ANC REF MIC):
MIC2/4 BIAS,
MIC2_P,_N

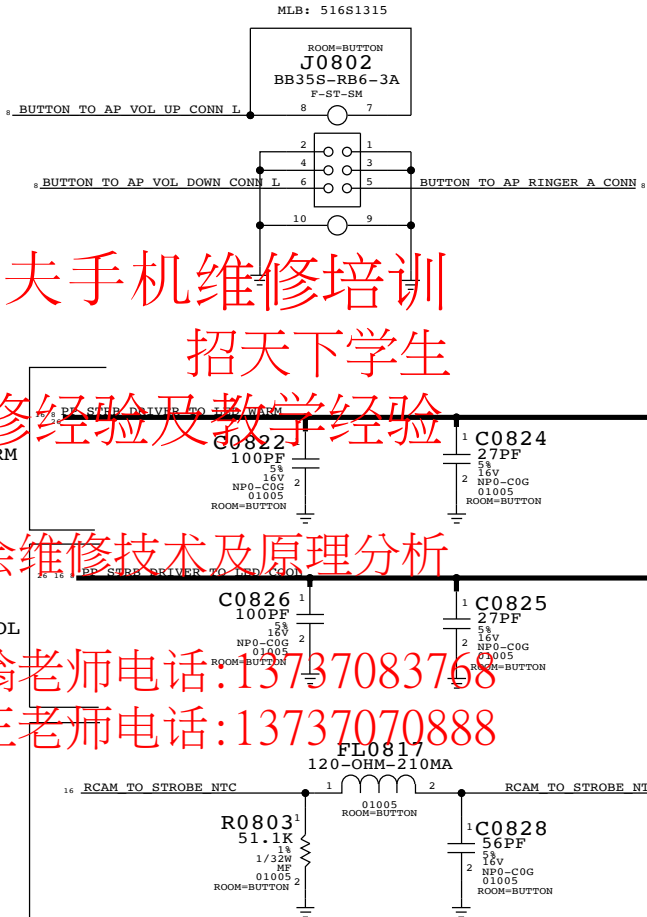
BUTTONS:
RINGER, HOLD,
VOL_UP/DOWN,



RIGHT BUTTON B2B



LEFT BUTTON B2B



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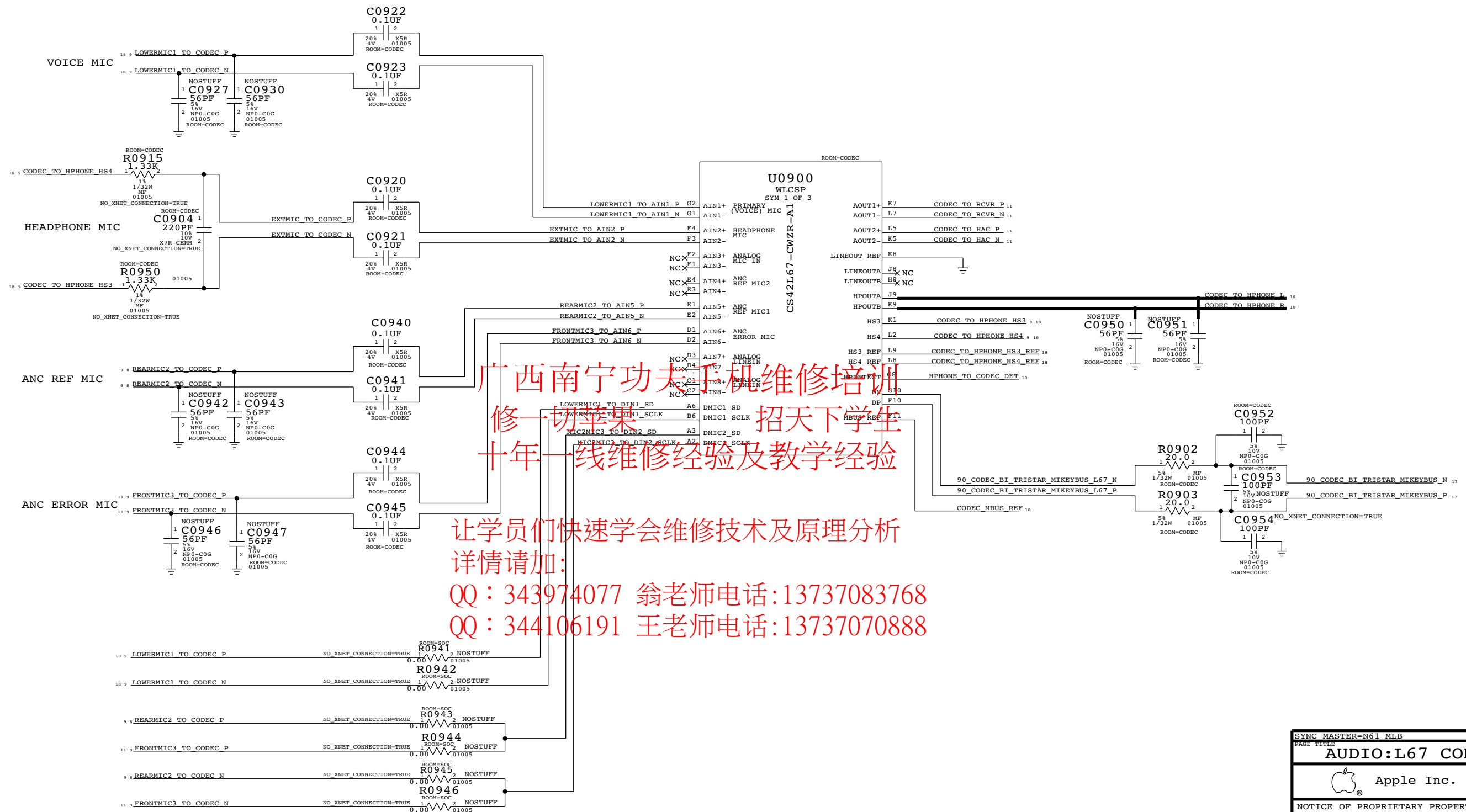
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SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
IO:BUTTON FLEX CONN			
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	REVISION	7.0.0	
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L67 AUDIO CODEC

AUDIO I/O

(ANALOG MIC IN, DIG MIC IN, HPOUT, LINEOUT, RECEIVER OUT, MIKEYBUS)



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SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
AUDIO:L67 CODEC (1/2)			
	DRAWING NUMBER		SIZE
	051-9903		D
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C

C

B

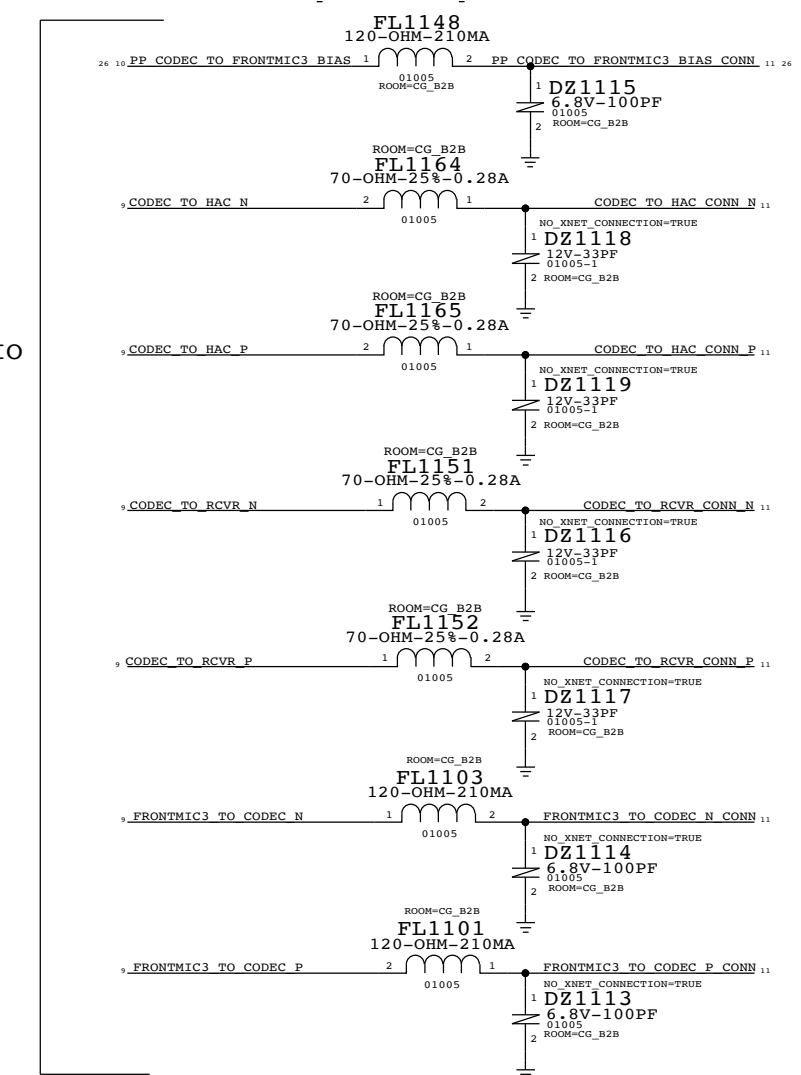
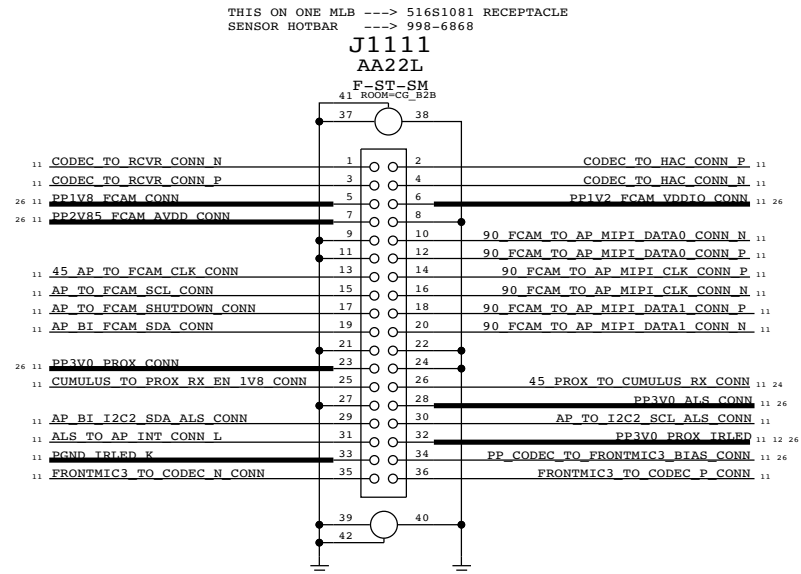


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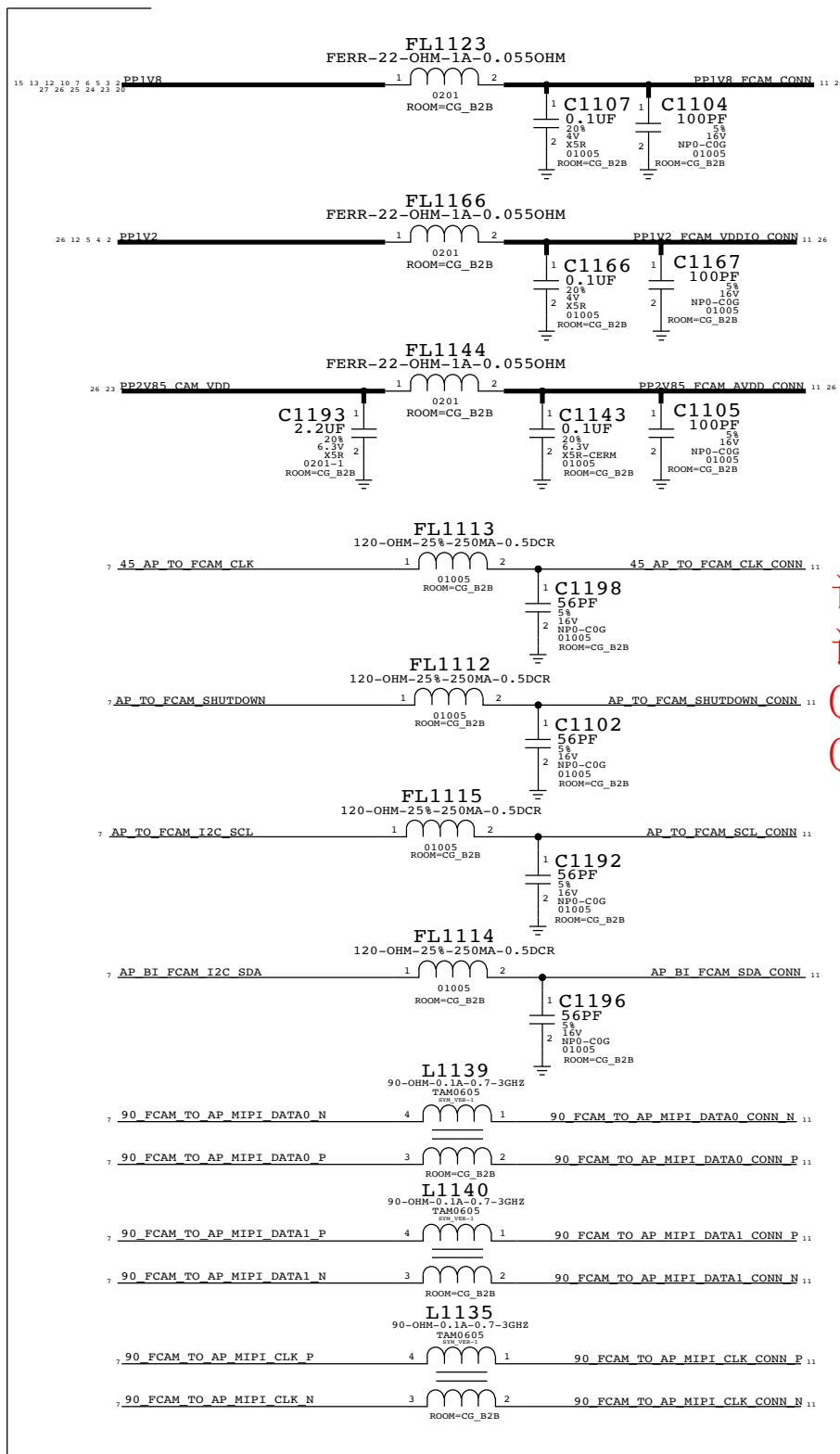
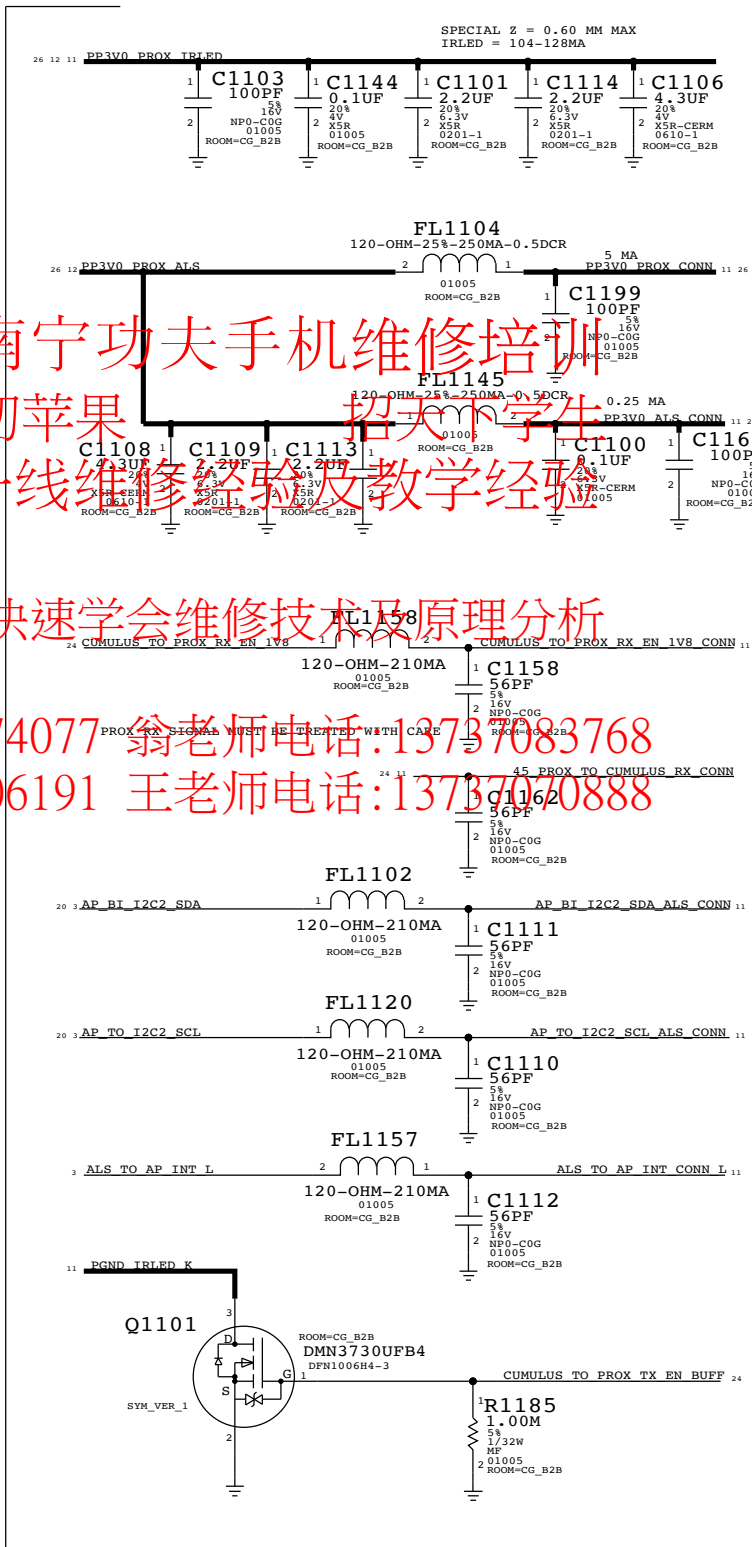
FRONT CAM FLEX B2B

(FCAM, PROX, ALS, RECEIVER, ANC ERROR MIC)



SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
CAMERA: FRONT FLEX CONN			
 Apple Inc.	DRAWING NUMBER	051-9903	SIZE D
	REVISION	7.0.0	
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QQ: 344106191 王老师电话: 13737070888



ADI PMU

(BUCK, LDO, VIBE DRIVER, 32K, CHARGER)

NOTE: L1210, L1212 BOMPTIONS
CONTROLLED ON PAGE1

APN: 338S1251 (ADI AZ)

U1202

D2186AZE0FJAVAC

FCCSP-N56-N61 ROOM=PMU

SYM 1 OF 3

VBUS_OVP_OFF

VCENTER

VBUS

IBAT

VBAT

ACT_DIO

CHG_LX

VCC_MAIN

VCC_MAIN_S

VDD_BUCK1

VDD_BUCK2

VDD_BUCK3

VDD_BUCK4

VDD_BUCK5

VDD_BUCK6

VDD_BYP_BUCK6

VDD_BUCK001

VDD_BUCK023

VDD_LDO6

VDD_LDO2

VDD_LDO1_3

VDD_LDO4_13

VDD_LDO5

VDD_LDO7_8

VDD_LDO10

VDD_LDO9_11

VDD_VIB

VIB

VIB_PWM_EN

XTAL1

XTAL2

VSS_RTC

VDD_LDO6

VDD_LDO2

VDD_LDO1_3

VDD_LDO4_13

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VDD_LDO7_8

VDD_LDO10

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VDD_VIB

VIB

VIB_PWM_EN

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XTAL2

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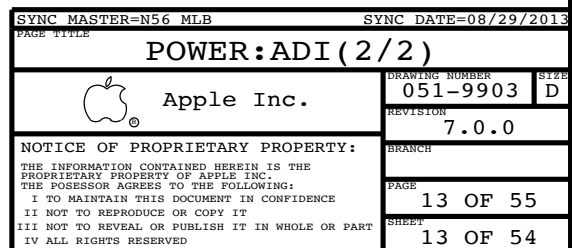
VIB_PWM_EN

XTAL1

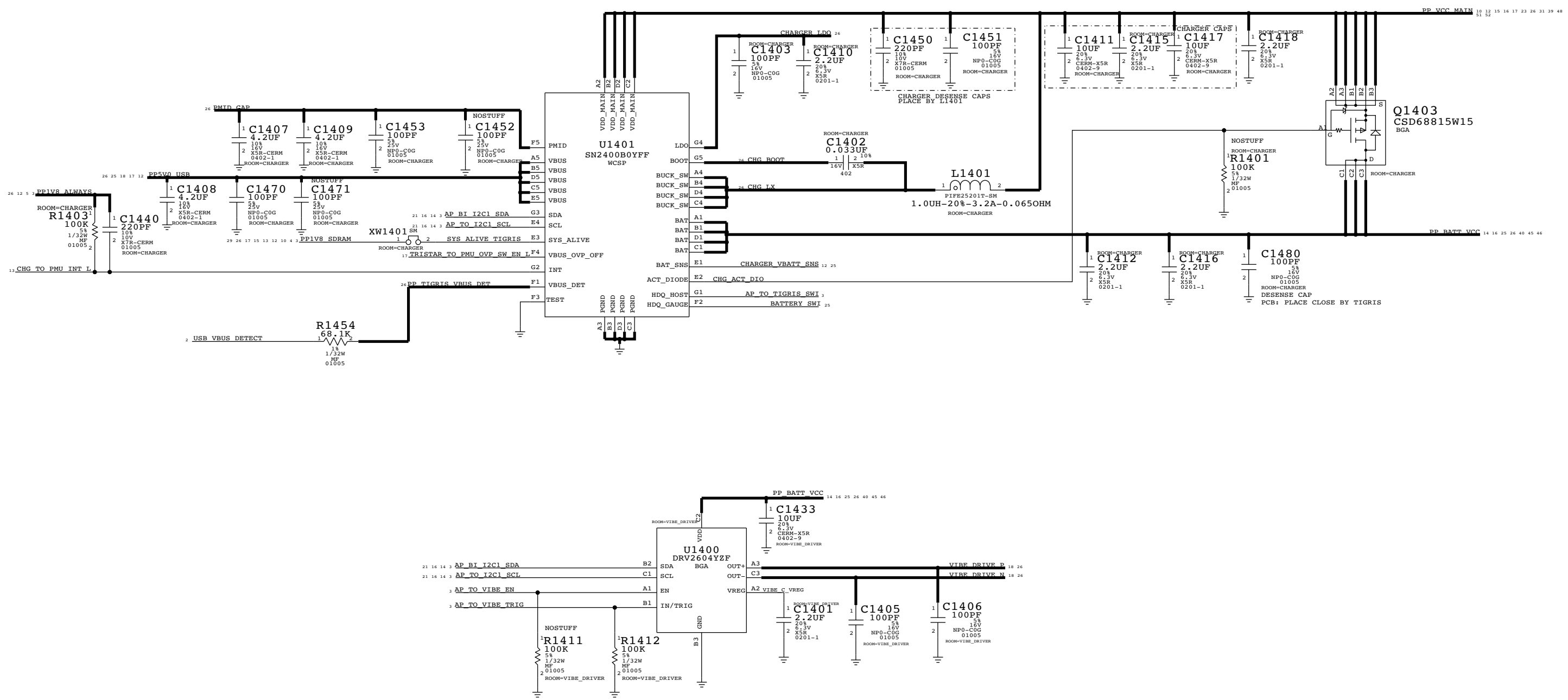
XTAL2

VSS_RTC

```
(AMUX, GPIO, BUTTONS, ADC, THERMISTORS, SYSTEM I/F, GND)
```

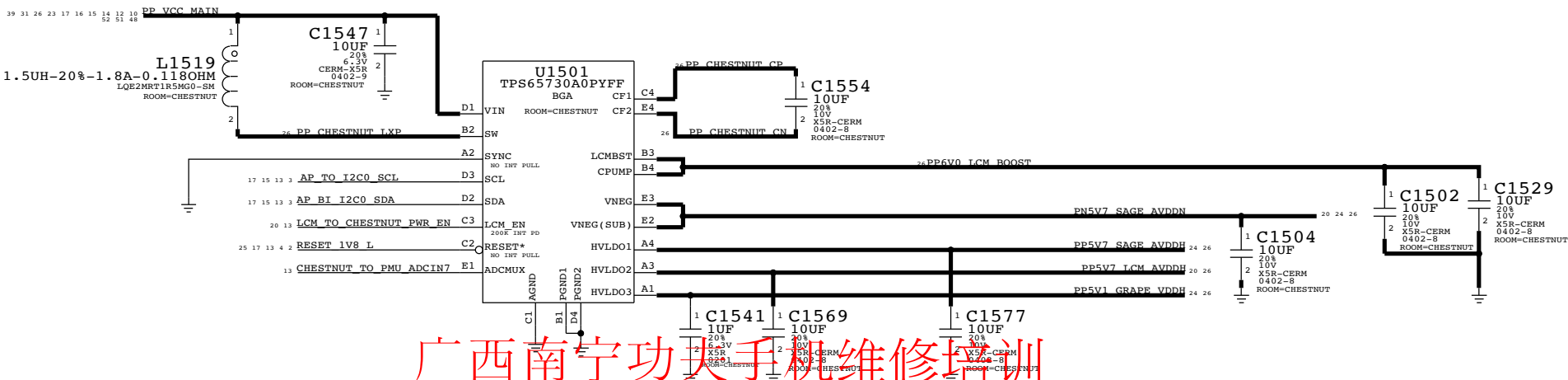


TIGRIS CHARGER & VIBE DRIVER



CHESTNUT, BACKLIGHT DRIVER, MESA BOOST

D500 DISPLAY PMU (TI CHESTNUT, 338S1149)

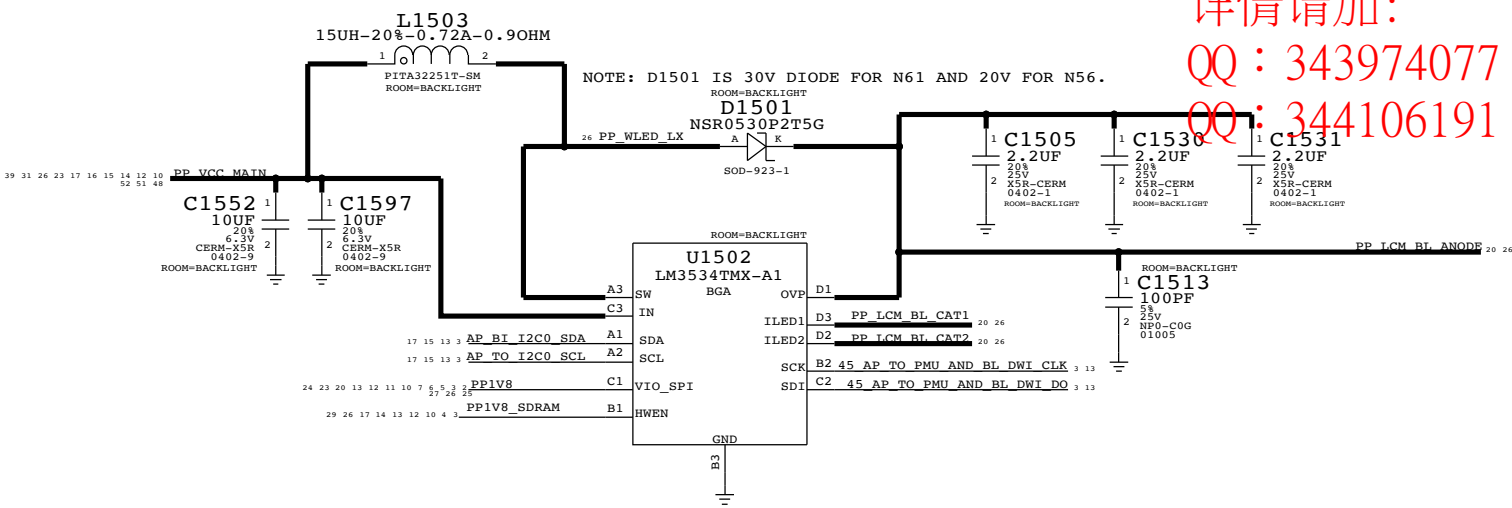


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修一切苹果 招天下学生
十年一线维修经验及教学经验

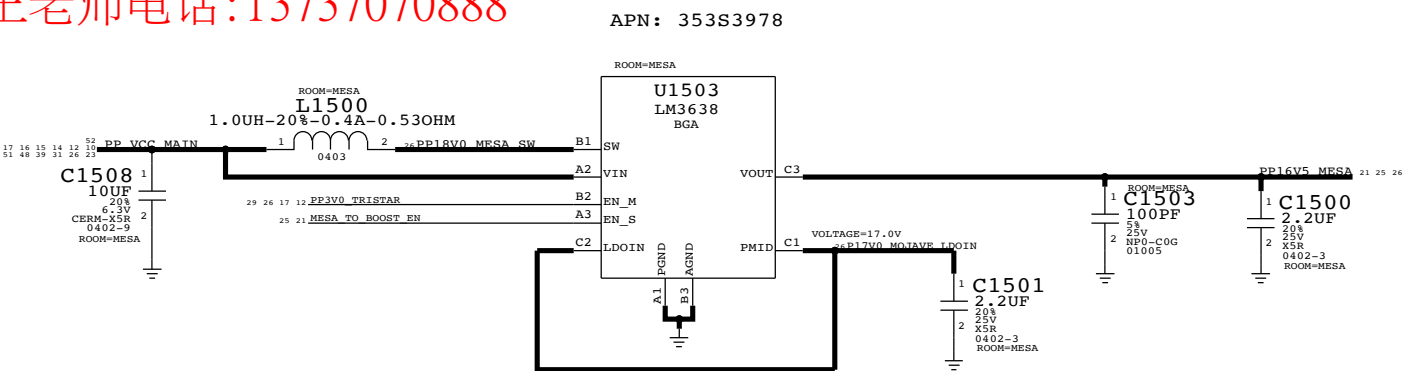
D500 BACKLIGHT DRIVER

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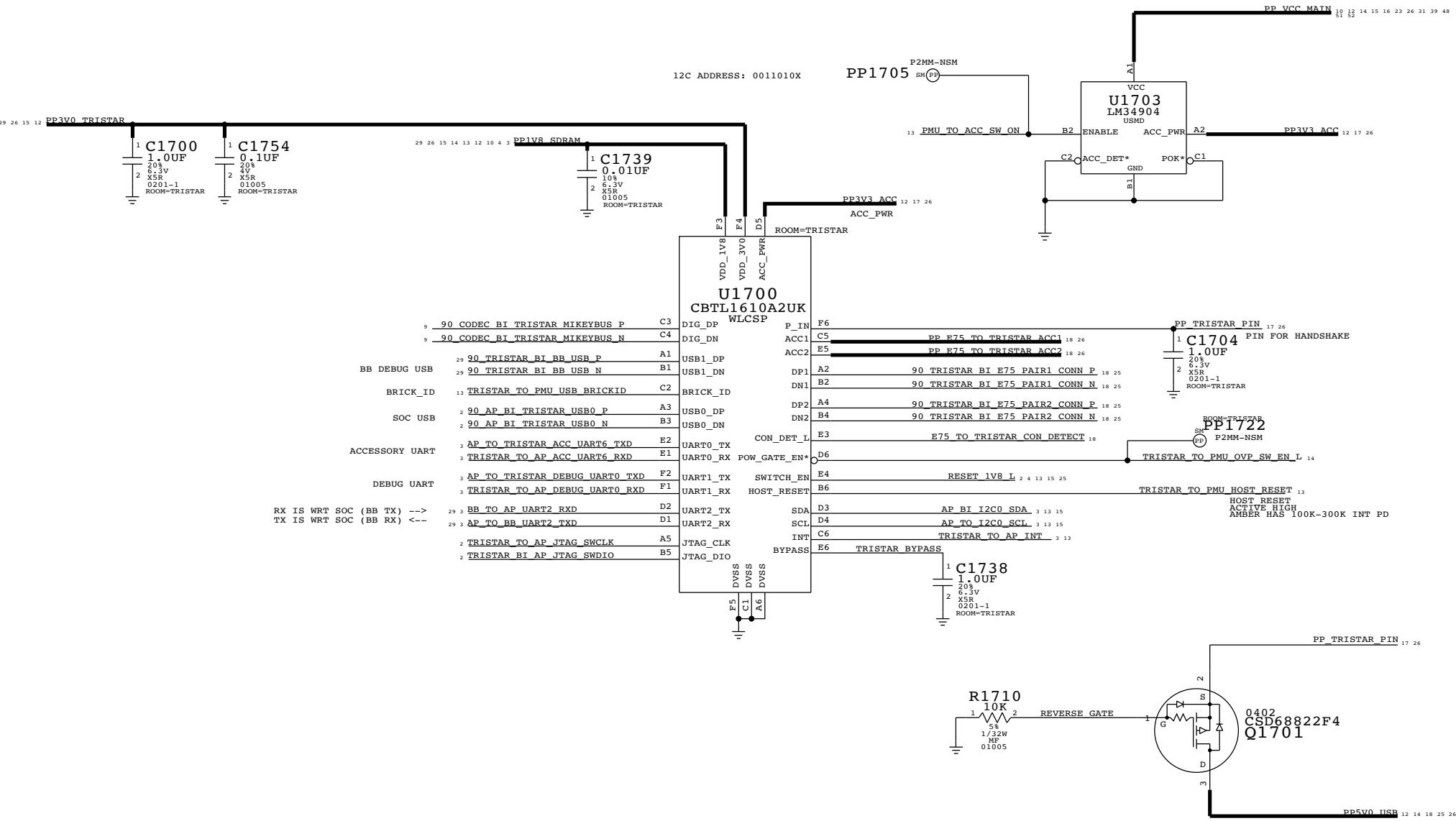


MESA BOOST A0



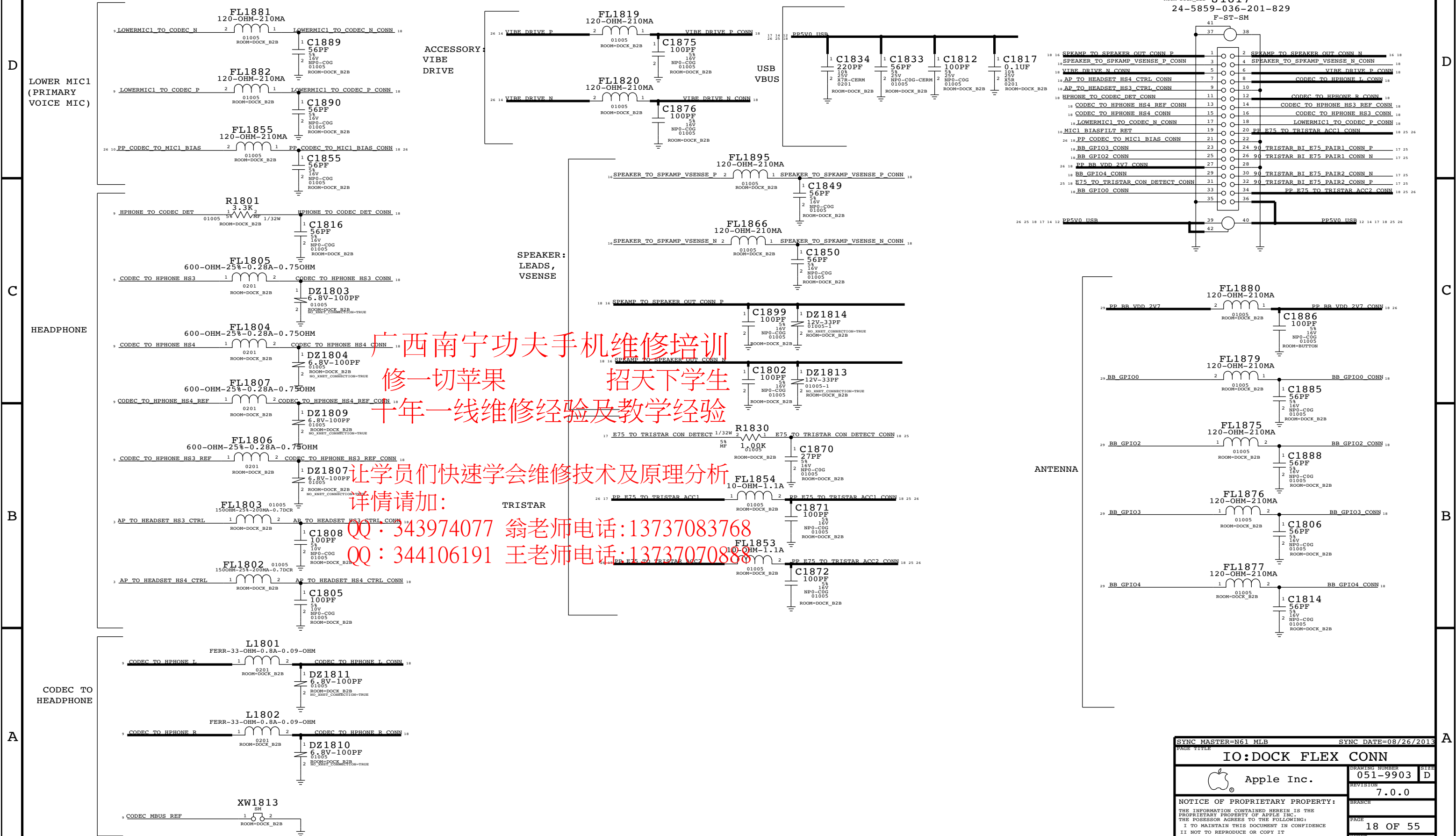
PAGE TITLE		SYNC DATE=08/26/2013	
DISPLAY: CHESTNUT, BACKLIGHT DRIVER		DRAWING NUMBER	051-9903
Apple Inc.		REVISION	7.0.0
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TRISTAR2



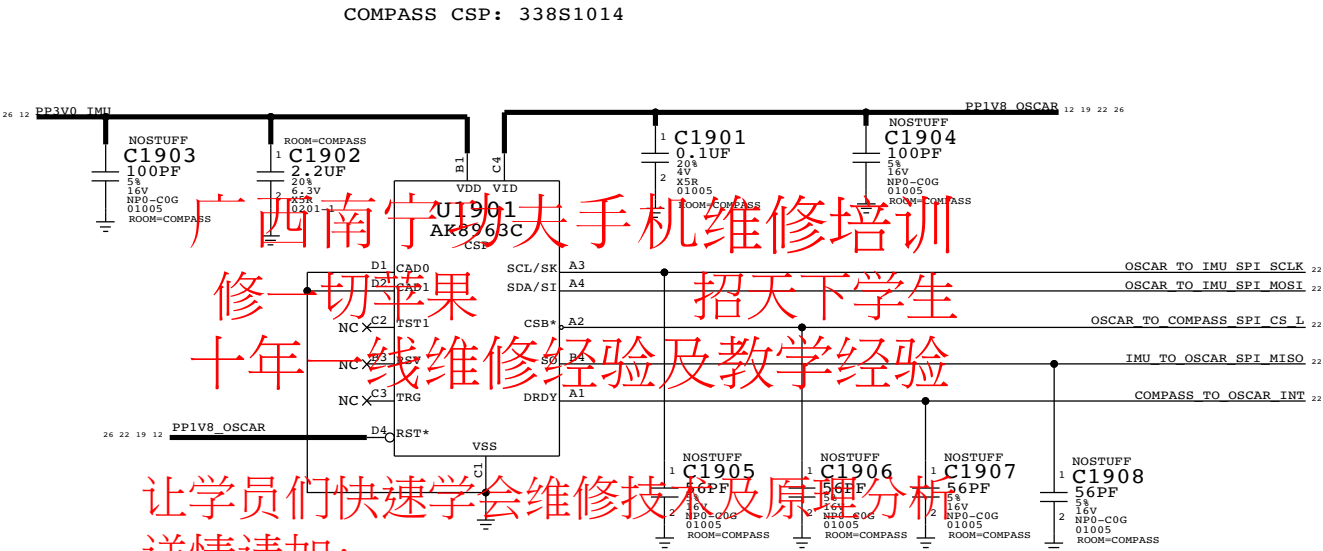
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PAGE TITLE			
IO:TRISTAR2			
 Apple Inc.		DRAWING NUMBER	051-9903
		SIZE	D
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		BRANCH	
		PAGE	17 OF 55
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DOCKFLEX B2B (USB VBUS, SPEAKER, ANTENNA LAT SW CTRL, MIC1 (PRIMARY MIC), ACC DET/ID/PWR, E75 DIFFPAIRS)



SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
IO:DOCK FLEX CONN			
 Apple Inc.		DRAWING NUMBER	051-9903
		SIZE	D
REVISION		7.0.0	
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COMPASS – AKM COMPASS IN POR LOCATION



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修一切苹果 招天下学生
十年一线维修经验及教学经验

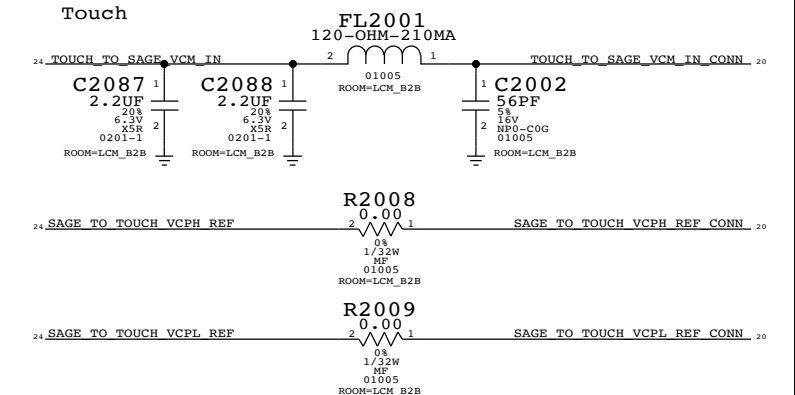
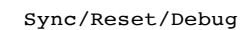
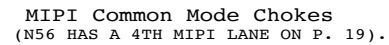
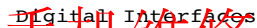
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(N56 HAS A 2ND SET OF BL SIGNALS ON P. 19).



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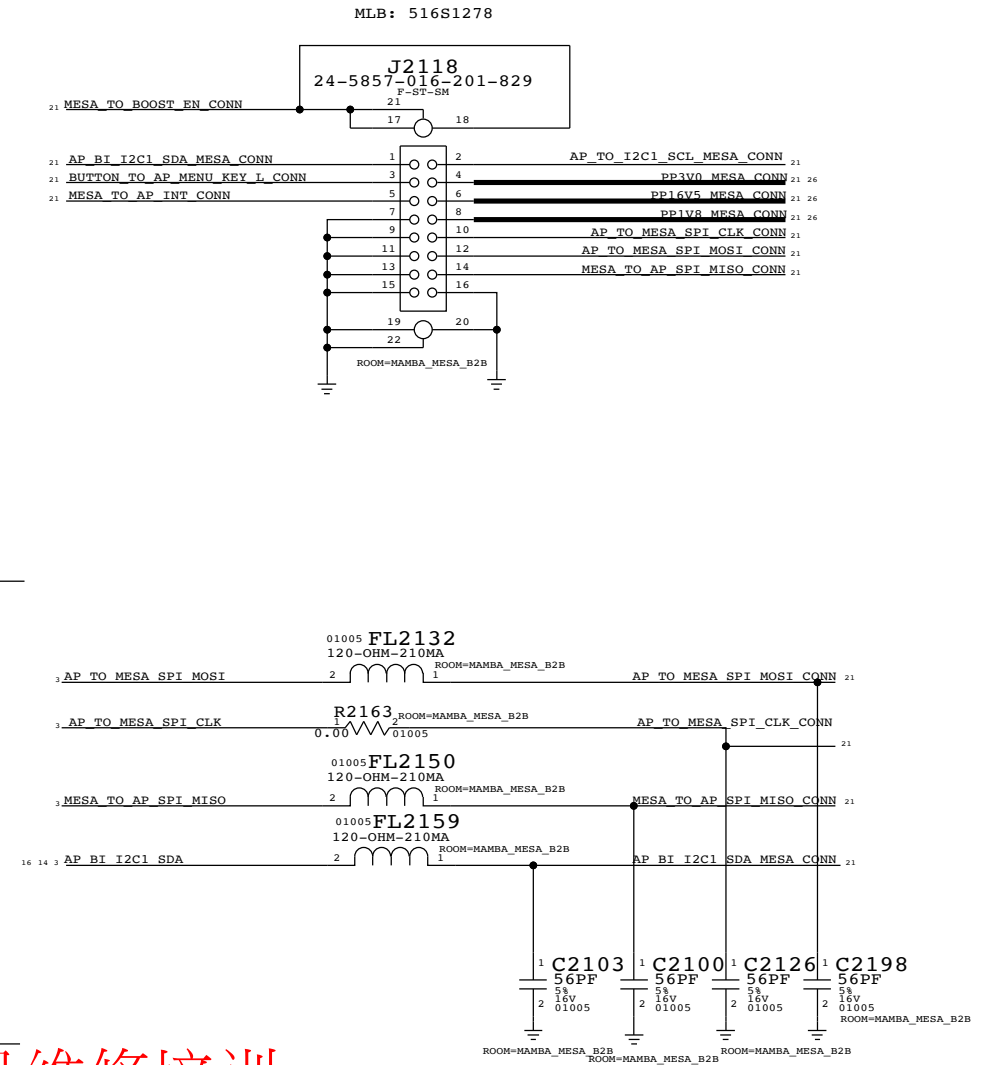
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		20 OF 55	
		SHEET	
		20 OF 54	

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SENSORS:MESA FLEX CONN		051-9903		D	
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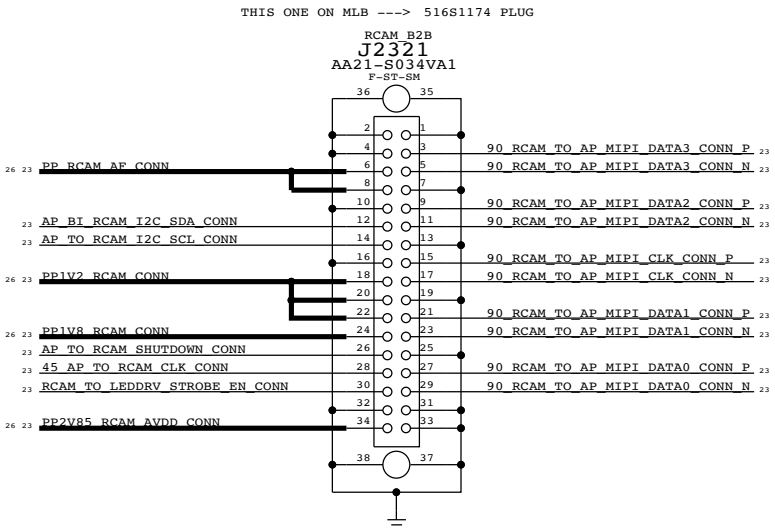
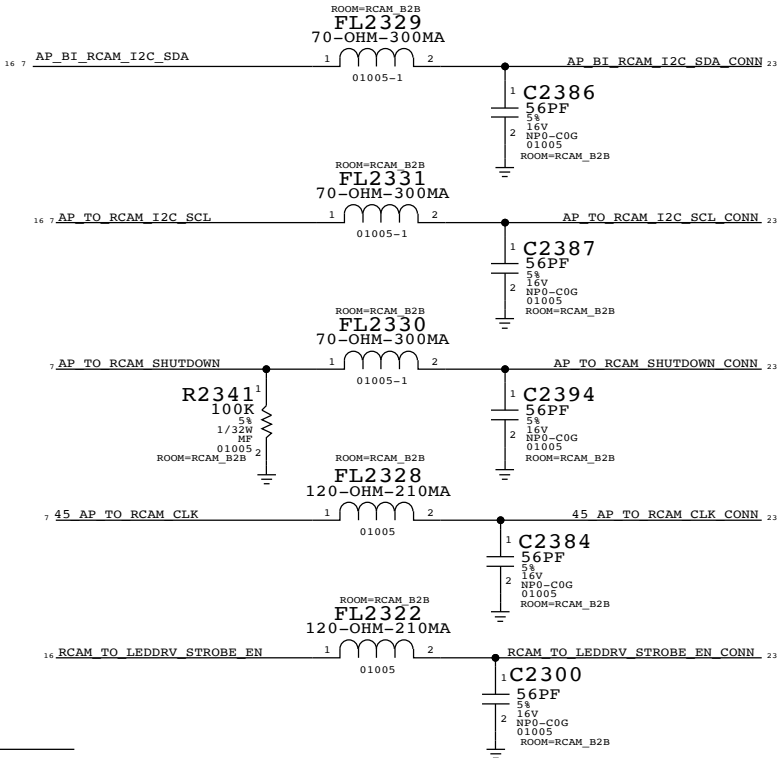
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RCAM B2B (REAR CAMERA CONNECTOR)

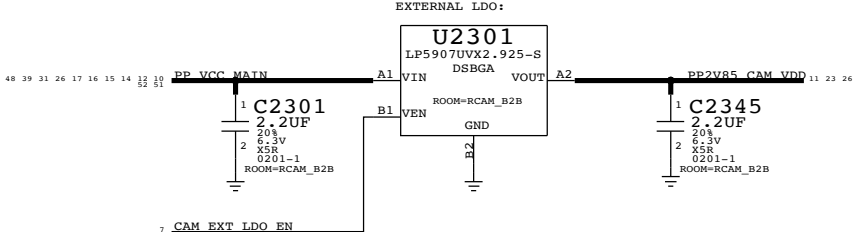
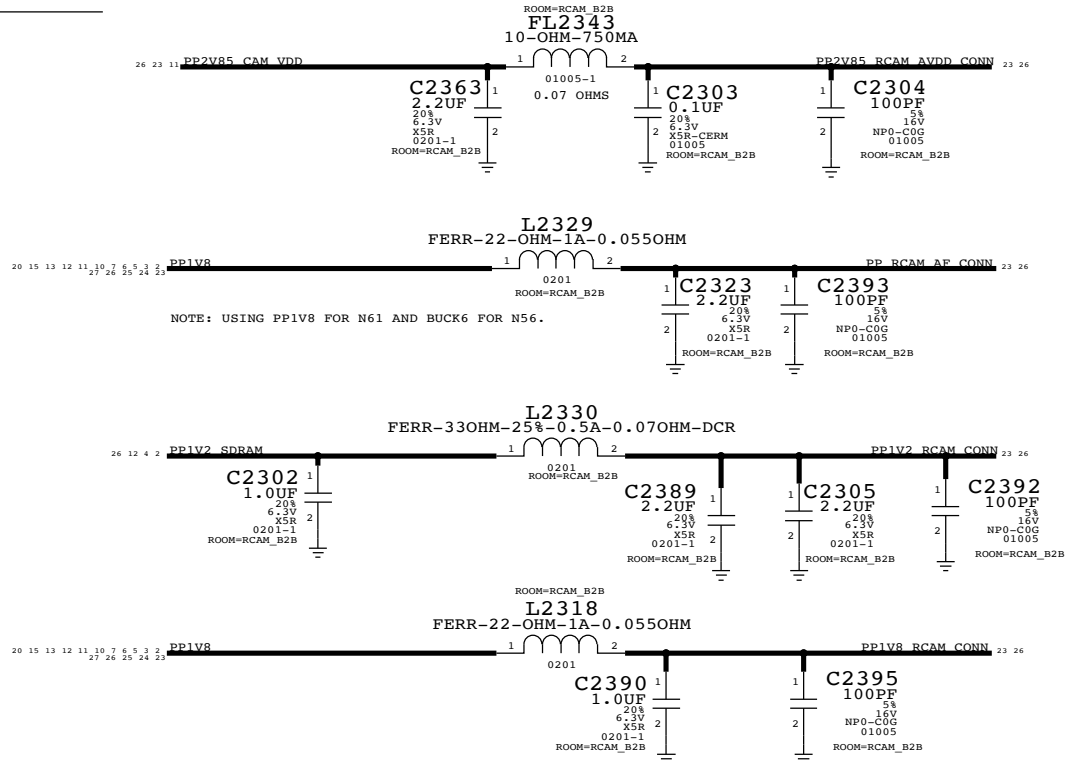
RCAM:
4-LANE MIPI

RCAM:
DIGITAL I/F
(I2C, CTRL, CLK)

RCAM:
POWER:
(1.8V DVDD)
(2.8V AVDD)
(1.2V VCC)
(1.8V/2V AF)



RCAM/FCAM AVDD RAIL EXT. LDO:



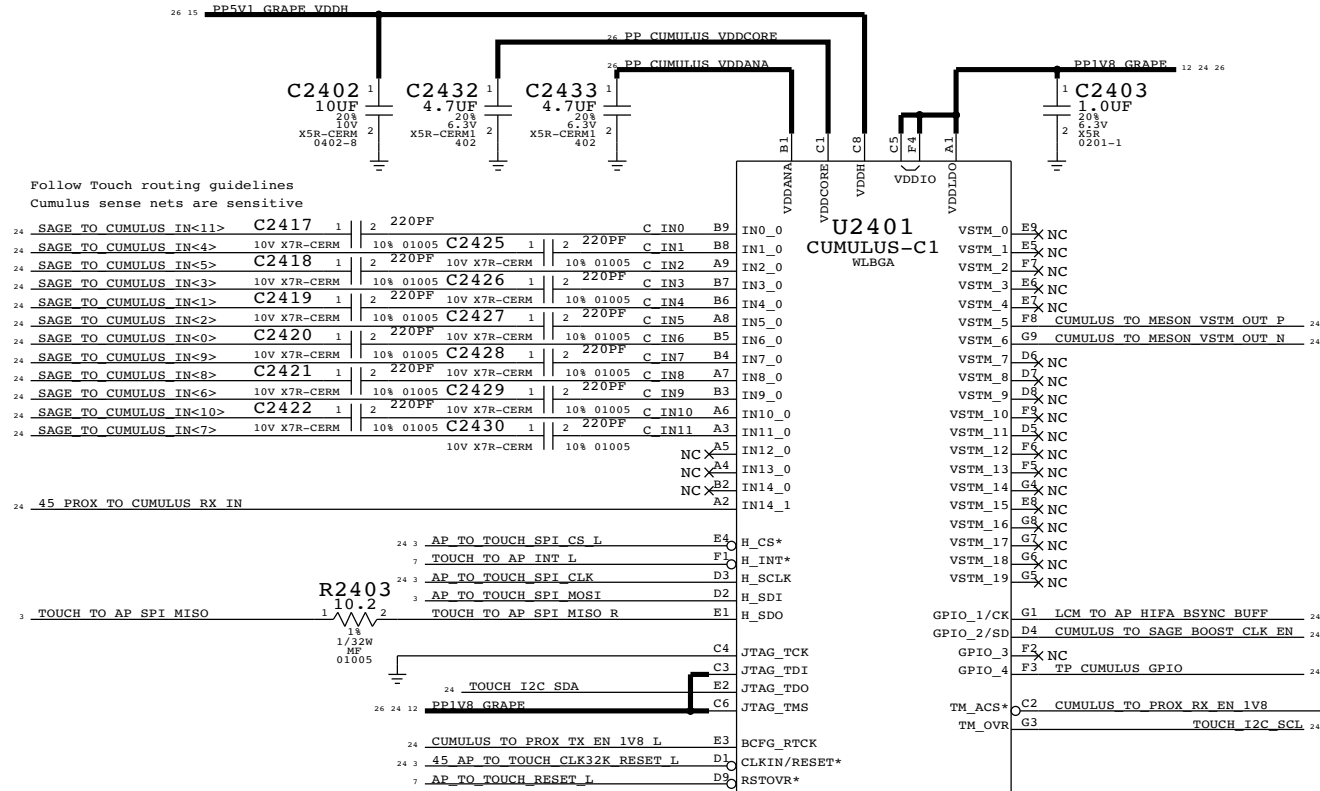
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Touch (B2B, Driver ICs)

Cumulus

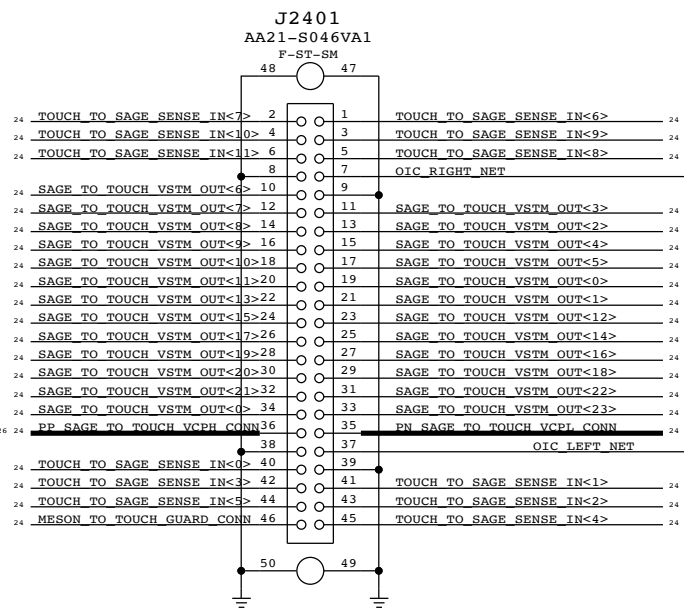
APN: 343S0638

Turn on is later than PPIV8_GRAPE
Turn off is same time as PPIV8_GRAPE

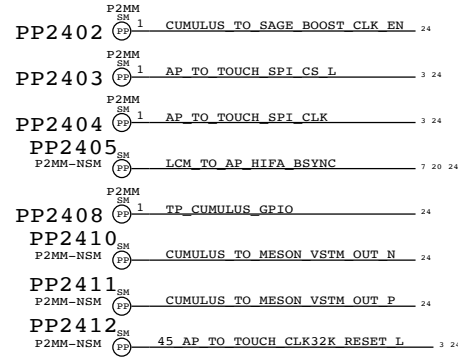


Touch B2B

MLB APN : 516S1086 (Receptacle)
Flex APN: 516S1087 (Plug)

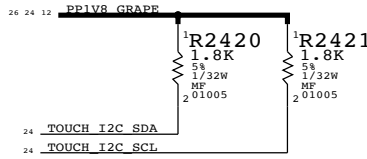


Touch probe points



Tantalums solved singing caps
issue. Validate issue is resolved
with Meson and replace with
0402 ceramics.

I2C pull-ups



8	7	6	5	4	3	2	1
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SYNC MASTER=N61 MLB SYNC DATE=08/26/2013

POWER: BATT CONN, TPS, PD FEATURES		DRAWING NUMBER 051-9903		SIZE D	
 Apple Inc.		REVISION			
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		SHEET 25 OF 54			

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PAGE TITLE		DRAWING NUMBER		SIZE
SYSTEM:N61 SPECIFIC		051-9903		D
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RADIO_MLB HIERARCHICAL SYMBOL

VOLTAJE DE 1.8 SE CONVIERTE EN RFFE2 PARA LA RF

POWER

VCC_MAIN, VBAT GOES TO RADIO_MLB DIRECTLY
CHECK ALL PAGES IN RF SIDE!

CELLULAR HOUSE KEEPING

3	AP_TO_RADIO_ON_L	MAKE_BASE+TRUE	I325	RADIO_ON_L	30 32
3	BB_TO_AP_RESET_DET_L	MAKE_BASE+TRUE	I324	BB_RESET_DET_L	30 35
13	PMU_TO_BB_RST_L	MAKE_BASE+TRUE	I326	RF_PMIC_RESET_L	30 32
3	AP_TO_BB_RST_L	MAKE_BASE+TRUE	I327	BB_RST_L	30 32
3	AP_TO_BB_WAKE_MODEM	MAKE_BASE+TRUE	I329	AP_WAKE_MODEM	35
13	BB_TO_PMU_HOST_WAKE_L	MAKE_BASE+TRUE	I328	BB_WAKE_HOST_L	30 35
3	BB_TO_AP_IPC_GPIO	MAKE_BASE+TRUE	I331	BB_IPC_GPIO	35
16	BB_TO_LEDDRV_GSM_BLANK	MAKE_BASE+TRUE	I330	GSM_TXBURST_IND	35
3	BB_TO_AP_GPS_SYNC	MAKE_BASE+TRUE	I332	BB_GPS_SYNC	30 35

HSIC IPC

2	50_AP_BI_BB_HSIC1_DATA	MAKE_BASE+TRUE	I368	50_BB_HSIC_DATA	30 34
3	50_AP_BI_BB_HSIC1_STB	MAKE_BASE+TRUE	I369	50_BB_HSIC_STROBE	30 34
3	AP_TO_BB_HOST_RDY	MAKE_BASE+TRUE	I371	BB_HOST_RDY	30 35
3	BB_TO_AP_DEVICE_RDY	MAKE_BASE+TRUE	I370	BB_DEVICE_RDY	30 35
3	BB_TO_AP_IPC_GPIO1	MAKE_BASE+TRUE	I372	BB_IPC_GPIO1	35

UART IPC

3	AP_TO_BB_UART2_RTS_L	MAKE_BASE+TRUE	I373	BB_UART_CTS_L	30 35
3	BB_TO_AP_UART2_CTS_L	MAKE_BASE+TRUE	I376	BB_UART_RTS_L	30 35
17 3	AP_TO_BB_UART2_TXD	MAKE_BASE+TRUE	I374	BB_UART_RXD	30 35
17 3	BB_TO_AP_UART2_RXD	MAKE_BASE+TRUE	I375	BB_UART_TXD	30 35

AUDIO I2S

3	45_AP_TO_BB_I2S3_BCLK	MAKE_BASE+TRUE	I377	BB_I2S_CLK	35
3	AP_TO_BB_I2S3_DOUT	MAKE_BASE+TRUE	I378	BB_I2S_RXD	30 35
3	BB_TO_AP_I2S3_DIN	MAKE_BASE+TRUE	I379	BB_I2S_TXD	30 35
3	AP_TO_BB_I2S3_LRCLK	MAKE_BASE+TRUE	I380	BB_I2S_WS	30 35

OSCAR UART

22	OSCAR_TO_BB_UART_TXD	MAKE_BASE+TRUE	I382	BB_OTHER_RXD	30 35
22	BB_TO_OSCAR_UART_RXD	MAKE_BASE+TRUE	I381	BB_OTHER_TXD	30 35

BB DEBUG INTERFACES

3	AP_TO_BB_COREDUMP	MAKE_BASE+TRUE	I384	BB_CORE_DUMP	30 35
13	PMU_TO_BB_VBUS_DET	MAKE_BASE+TRUE	I387	BB_USB_VBUS	30 34
17	90_TRISTAR_BI_BB_USB_N	MAKE_BASE+TRUE	I386	90_BB_USB_N	30 34
17	90_TRISTAR_BI_BB_USB_P	MAKE_BASE+TRUE	I388	90_BB_USB_P	30 34

RADIO ANTENNA CONTROL

18	PP_BB_VDD_2V7	MAKE_BASE+TRUE	I389	PP_LDO14_RFSW	31 41 42
18	BB_GPIO0	MAKE_BASE+TRUE	I390	BB_LAT_GPIO0	35
18	BB_GPIO2	MAKE_BASE+TRUE	I391	BB_LAT_GPIO2	35
18	BB_GPIO3	MAKE_BASE+TRUE	I392	BB_LAT_GPIO3	35
18	BB_GPIO4	MAKE_BASE+TRUE	I394	BB_LAT_GPIO4	35

FCT TESTING

13	RADIO_TO_PMU_ADC_SMPS1	MAKE_BASE+TRUE	I395	ADC_SMPS1	30
13	RADIO_TO_PMU_ADC_PP_LDO11_VDDIO	MAKE_BASE+TRUE	I396	ADC_PP_LDO11	30
13	RADIO_TO_PMU_ADC_PP_LDO5_SIM	MAKE_BASE+TRUE	I398	ADC_PP_LDO5	30
13	RADIO_TO_PMU_ADC_SMPS4	MAKE_BASE+TRUE	I397	ADC_SMPS4	30

UPPER RADIO ANTENNA CONTROL

25	50_AP_WIFI_5G_CONN_ANT	MAKE_BASE+TRUE	I410	50_WIFI_5G_CONN_ANT	50
25	50_AP_UAT_FEED	MAKE_BASE+TRUE	I409	50_UPPER_ANT_FEED	50
	UAT_ANT_GND	MAKE_BASE+TRUE	I411	ANT_GND	50
29 26 17 15 12	PP3V0_TRISTAR	MAKE_BASE+TRUE	I404	PAC_VDD_3V0	53
25 8	NORTH_AC_GND_SCREW	MAKE_BASE+TRUE	I412	NORTH_ANT_GND	50

POWER



WLAN/BT HOUSE KEEPING

13	45_PMU_TO_WLAN_CLK32K	MAKE_BASE+TRUE	I316	CLK32K_AP	30 51
13	PMU_TO_WLAN_REG_ON	MAKE_BASE+TRUE	I317	WLAN_REG_ON	30 51
13	WLAN_TO_PMU_HOST_WAKE	MAKE_BASE+TRUE	I318	HOST_WAKE_WLAN	30 51
13	PMU_TO_BT_REG_ON	MAKE_BASE+TRUE	I319	BT_REG_ON	30 51
3	AP_TO_BT_WAKE	MAKE_BASE+TRUE	I320	WAKE_BT	30 51
13	BT_TO_PMU_HOST_WAKE	MAKE_BASE+TRUE	I321	HOST_WAKE_BT	51

3	AP_TO_WLAN_JTAG_SWCLK	MAKE_BASE+TRUE	I333	WLAN_JTAG_SWCLK	30 51
3	AP_TO_WLAN_JTAG_SWDIO	MAKE_BASE+TRUE	I334	WLAN_JTAG_SWDIO	30 51
13	WLAN_TO_PMU_PCIE_WAKE_L	MAKE_BASE+TRUE	I335	WLAN_PCIE_WAKE_L	30 51
3	AP_TO_WLAN_DEVICE_WAKE	MAKE_BASE+TRUE	I336	PCIE_DEV_WAKE	30 51
3	90_WLAN_TO_AP_PCIE1_RXDP_P	MAKE_BASE+TRUE	I337	90_WLAN_PCIE_TDP	30 51
3	90_WLAN_TO_AP_PCIE1_RXDP_N	MAKE_BASE+TRUE	I340	90_WLAN_PCIE_TDN	30 51
3	90_AP_TO_WLAN_PCIE1_TXDP_P	MAKE_BASE+TRUE	I338	90_WLAN_PCIE_RDP	30 51
3	90_AP_TO_WLAN_PCIE1_TXDP_N	MAKE_BASE+TRUE	I339	90_WLAN_PCIE_RDN	30 51
3	90_AP_TO_WLAN_PCIE1_REFCLK1_P	MAKE_BASE+TRUE	I342	90_WLAN_PCIE_REFCLK_P	51
3	90_AP_TO_WLAN_PCIE1_REFCLK1_N	MAKE_BASE+TRUE	I341	90_WLAN_PCIE_REFCLK_N	51
7	WLAN_TO_AP_PCIE1_CLKREQ_L	MAKE_BASE+TRUE	I344	WLAN_PCIE_CLKREQ_L	30 51
7	AP_TO_WLAN_PCIE1_RST_L	MAKE_BASE+TRUE	I343	WLAN_PCIE_PERST_L	30 51

WLAN HSIC IPC

3	WLAN_TO_AP_UART4_RXD	MAKE_BASE+TRUE	I345	WLAN_UART_TXD	30 51
3	AP_TO_WLAN_UART4_TXD	MAKE_BASE+TRUE	I348	WLAN_UART_RXD	30 51
3	WLAN_TO_AP_UART4_CTS_L	MAKE_BASE+TRUE	I347	WLAN_UART_RTS_L	30 51
3	AP_TO_WLAN_UART4_RTS_L	MAKE_BASE+TRUE	I346	WLAN_UART_CTS_L	30 51

BT UART IPC

3	AP_TO_BT_UART1_RTS_L	MAKE_BASE+TRUE	I349	BT_UART_CTS_L	51
3	BT_TO_AP_UART1_CTS_L	MAKE_BASE+TRUE	I352	BT_UART_RTS_L	51
3	AP_TO_BT_UART1_TXD	MAKE_BASE+TRUE	I351	BT_UART_RXD	30 51
3	BT_TO_AP_UART1_RXD	MAKE_BASE+TRUE	I350	BT_UART_TXD	30 51

BT AUDIO PCM

3	45_AP_TO_BT_I2S1_BCLK	MAKE_BASE+TRUE	I354	BT_PCM_CLK	51
3	AP_TO_BT_I2S1_DOUT	MAKE_BASE+TRUE	I353	BT_PCM_IN	51
3	BT_TO_AP_I2S1_DIN	MAKE_BASE+TRUE	I355	BT_PCM_OUT	51
3	AP_TO_BT_I2S1_LRCLK	MAKE_BASE+TRUE	I356	BT_PCM_SYNC	51

OSCAR STATES

22	OSCAR_TO_RADIO_CONTEXT_A	MAKE_BASE+TRUE	I358	OSCAR_CONTEXT_A	51
22	OSCAR_TO_RADIO_CONTEXT_B	MAKE_BASE+TRUE	I357	OSCAR_CONTEXT_B	51

STOCKHOLM

3	STOCKHOLM_TO_AP_UART3_CTS_L	MAKE_BASE+TRUE	I359	STOCKHOLM_RTS_L	30 52
3	AP_TO_STOCKHOLM_UART3_RTS_L	MAKE_BASE+TRUE	I360	STOCKHOLM_CTS_L	30 52
3	STOCKHOLM_TO_AP_UART3_RXD	MAKE_BASE+TRUE	I361	STOCKHOLM_UART_TXD	30 52
3	AP_TO_STOCKHOLM_UART3_TXD	MAKE_BASE+TRUE	I363	STOCKHOLM_UART_RXD	30 52
3	AP_TO_STOCKHOLM_DWLD_REQ	MAKE_BASE+TRUE	I362	STOCKHOLM_FW_DWLD_REQ	52
13	STOCKHOLM_TO_PMU_HOST_WAKE	MAKE_BASE+TRUE	I364	STOCKHOLM_HOST_WAKE	30 52
3	AP_TO_STOCKHOLM_EN	MAKE_BASE+TRUE	I365	STOCKHOLM_ENABLE	52
29 26 17 15 12	PP3V0_TRISTAR	MAKE_BASE+TRUE	I366	STOCKHOLM_VDD_MUX_3V0	54
3	AP_TO_STOCKHOLM_SIM_SEL	MAKE_BASE+TRUE	I367	STOCKHOLM_SIM_SEL	54
25	AP_TO_STOCKHOLM_ANT	MAKE_BASE+TRUE	I406	STOCKHOLM_ANT	52

PAGE TITLE		
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	REVISION	7.0.0
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	PAGE	30 OF 55
	SHEET	29 OF 54
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AP INTERFACE & DEBUG CONNECTORS

PROBE POINTS

PP3105 RF	P2MM	SM 1	CLK32K_AP	29 51
PP3113 RF	P4MM	SM 1	BB COEX UART_RXD	35 51
PP3114 RF	P4MM	SM 1	BB COEX UART_TXD	35 51
PP3119 RF	P2MM	SM 1	BT UART_TXD	29 51
PP3120 RF	P2MM-NSM	SM 1	BT UART_RXD	29 51
PP3152 RF	P2MM	SM 1	WAKE_BT	29 51
PP3153 RF	P2MM	SM 1	WLAN_REG_ON	29 51
PP3154 RF	P4MM	SM 1	BT_REG_ON	29 51
PP3155 RF	P2MM	SM 1	HOST_WAKE_WLAN	29 51
PP3156 RF	P2MM	SM 1	WLAN_PCIE_WAKE_L	29 51
PP3157 RF	P2MM	SM 1	WLAN_PCIE_PERST_L	29 51
PP3158 RF	P2MM	SM 1	WLAN_PCIE_CLKREQ_L	29 51
PP3159 RF	P4MM	SM 1	PCIE_DEV_WAKE	29 51
PP3160 RF	P2MM	SM 1	WLAN_UART_RTS_L	29 51
PP3161 RF	P2MM	SM 1	WLAN_UART_CTS_L	29 51
PP3162 RF	P2MM	SM 1	WLAN_UART_RXD	29 51
PP3163 RF	P2MM	SM 1	WLAN_UART_TXD	29 51
PP3190 RF	P2MM	SM 1	WLAN_JTAG_SWDCLK	29 51
PP3191 RF	P2MM	SM 1	WLAN_JTAG_SWDIO	29 51

PP3121 RF	P2MM-NSM	SM 1	STOCKHOLM_HOST_WAKE	29 52
PP3122 RF	P4MM	SM 1	BB_REQUEST_XO_CLK	32 52
PP3123 RF	P2MM-NSM	SM 1	STOCKHOLM_UART_RXD	29 52
PP3124 RF	P2MM	SM 1	STOCKHOLM_UART_TXD	29 52
PP3125 RF	P2MM-NSM	SM 1	STOCKHOLM_CTS_L	29 52
PP3126 RF	P2MM-NSM	SM 1	STOCKHOLM_RTS_L	29 52
PP3128 RF	P4MM	SM 1	PP_PN65_VCC_SIM	52
PP3144 RF	P4MM	SM 1	STOCKHOLM_SIM_SWP	52 54
PP3129 RF	P4MM	SM 1	REF_CLK_FROM_BB	32 52
PP3164 RF	P4MM	SM 1	DSDS_SIM_CLK	34 54
PP3183 RF	P4MM	SM 1	DSDS_SIM_RESET	34 54
PP3184 RF	P4MM	SM 1	DSDS_SIM_DATA	34 54
PP3185 RF	P4MM	SM 1	DSDS_SIM_DETECT	34
PP3187 RF	P4MM	SM 1	PP_LDO6	31 33 54
PP3188 RF	P4MM	SM 1	DSDS_SIM_SWP	54
PP3189 RF	P4MM	SM 1	DSDS_SIM_DATA_R	54
PP 3178 RF	P2MM-NSM	SM 1	BB_SIM_RESET	30 35
PP 3179 RF	P2MM-NSM	SM 1	BB_SIM_CLK	30 35
PP 3180 RF	P2MM-NSM	SM 1	BB_SIM_DATA	30 35
PP 3183 RF	P2MM-NSM	SM 1	BB_SIM_DETECT	30 35
PP 3184 RF	P2MM-NSM	SM 1	PP_LDO5	30 31 33 54

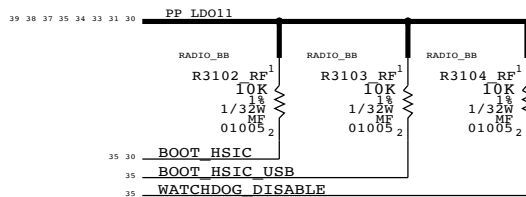
PP3115 RF	P4MM	SM 1	50_BB_HSIC_STROBE	29 34
PP3116 RF	P4MM-NSM	SM 1	50_BB_HSIC_DATA	29 34
PP3101 RF	P4MM	SM 1	BB_DEBUG_ERROR	35
PP3102 RF	P4MM	SM 1	RF_PMIC_RESET_L	29 32
PP3103 RF	P4MM	SM 1	PS_HOLD_PMIC	32
PP3127 RF	P4MM	SM 1	PMIC_RESOUT_L	32 34
PP3104 RF	P4MM	SM 1	MDM_CLK	32 34
PP3109 RF	P4MM	SM 1	PP_LDO11	30 31 33 34 35 37 38
PP3110 RF	P4MM	SM 1	RADIO_ON_L	29 32
PP3111 RF	P4MM	SM 1	SPMI_DATA	32 34
PP3112 RF	P4MM	SM 1	SPMI_CLK	32 34

PP3130 RF	P4MM	SM 1	BB_JTAG_RST_L	34
PP3131 RF	P4MM	SM 1	BB_JTAG_TCK	34
PP3132 RF	P4MM	SM 1	BB_JTAG_TMS	34
PP3133 RF	P4MM	SM 1	BB_JTAG_TDO	34
PP3134 RF	P4MM	SM 1	BB_JTAG_TDI	34
PP3135 RF	P4MM	SM 1	BB_JTAG_TRST_L	34
PP3136 RF	P4MM	SM 1	BB_DEBUG_STATUS	35
PP3137 RF	P4MM	SM 1	BB_CORE_DUMP	29 35
PP3138 RF	P4MM	SM 1	BB_USB_VBUS	29 34
PP3139 RF	P4MM	SM 1	90_BB_USB_N	29 34
PP3140 RF	P4MM	SM 1	90_BB_USB_P	29 34

PP3141 RF	P4MM	SM 1	BB_UART_TXD	29 35
PP3142 RF	P4MM	SM 1	BB_UART_RXD	29 35
PP3143 RF	P4MM	SM 1	BB_UART_RTS_L	29 35
PP3144 RF	P4MM	SM 1	BB_UART_CTS_L	29 35
PP3145 RF	P4MM	SM 1	BB_HOST_RDY	29 35
PP3146 RF	P4MM	SM 1	BB_DEVICE_RDY	29 35
PP3147 RF	P4MM	SM 1	BB_GPS_SYNC	29 35
PP3148 RF	P4MM	SM 1	BB_WAKE_HOST_L	29 35
PP3149 RF	P4MM	SM 1	BB_RESET_DET_L	29 35
PP3150 RF	P4MM	SM 1	BB_RST_L	29 32
PP3151 RF	P4MM	SM 1	BOOT_HSIC	30 35

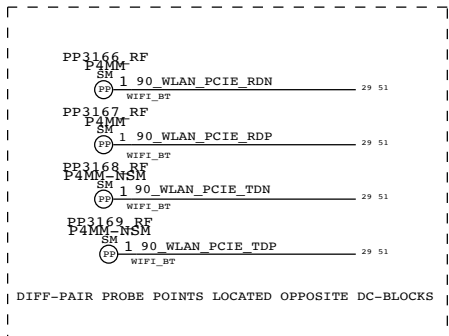
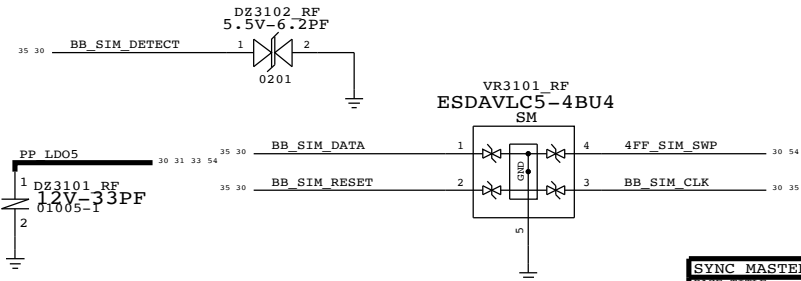
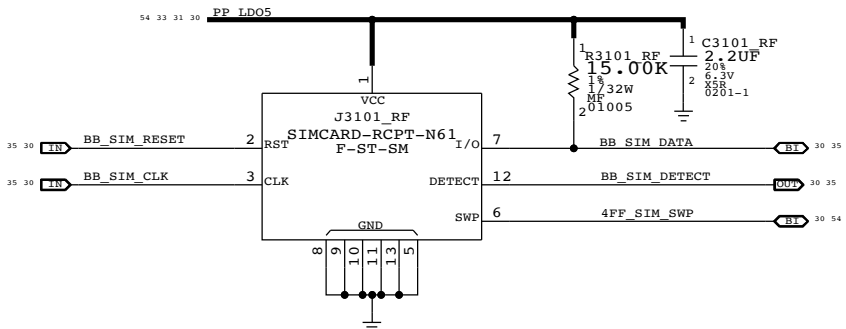
PP3170 RF	P4MM	SM 1	RF_FE1_CLK	35 39 40 41 42 43 44
PP3171 RF	P4MM	SM 1	RF_FE1_DATA	35 39 40 41 42 43 44
PP3172 RF	P4MM	SM 1	RF_FE2_CLK	35 45 46 48
PP3173 RF	P4MM	SM 1	RF_FE2_DATA	35 45 46 48
PP3175 RF	P4MM	SM 1	BB_I2S_WS	29 35
PP3176 RF	P4MM	SM 1	BB_I2S_RXD	29 35
PP3177 RF	P4MM	SM 1	BB_I2S_TXD	29 35
PP3178 RF	P4MM	SM 1	BB_OTHER_TXD	29 35
PP3179 RF	P4MM	SM 1	BB_OTHER_RXD	29 35

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
197S0565	197S0593	ALTERNATE	Y3301_RF	KDS 19.2MHZ XTAL
197S0598	197S0593	ALTERNATE	Y3301_RF	AVX 19.2MHZ XTAL
138S00005	138S00003	ALTERNATE	C3216_RF	150F CAPACITOR
138S0739	138S0706	ALTERNATE	C4207_RF	1.00UF CAPACITOR
138S0945	138S0706	ALTERNATE	C4207_RF	1.0UF CAPACITOR
138S1103	138S0719	ALTERNATE	C4007_RF	4.7UF CAPACITOR
339S0231	339S0228	ALTERNATE	U5201_RF	CORONA MODULE USI
339S0242	339S0228	ALTERNATE	U5201_RF	CORONA MODULE TDK
155S00024	155S0950	ALTERNATE	F_TRI_RF	TRIPLEXER BIN2



SIM CARD ESD PROTECTION

SIM CARD CONNECTOR

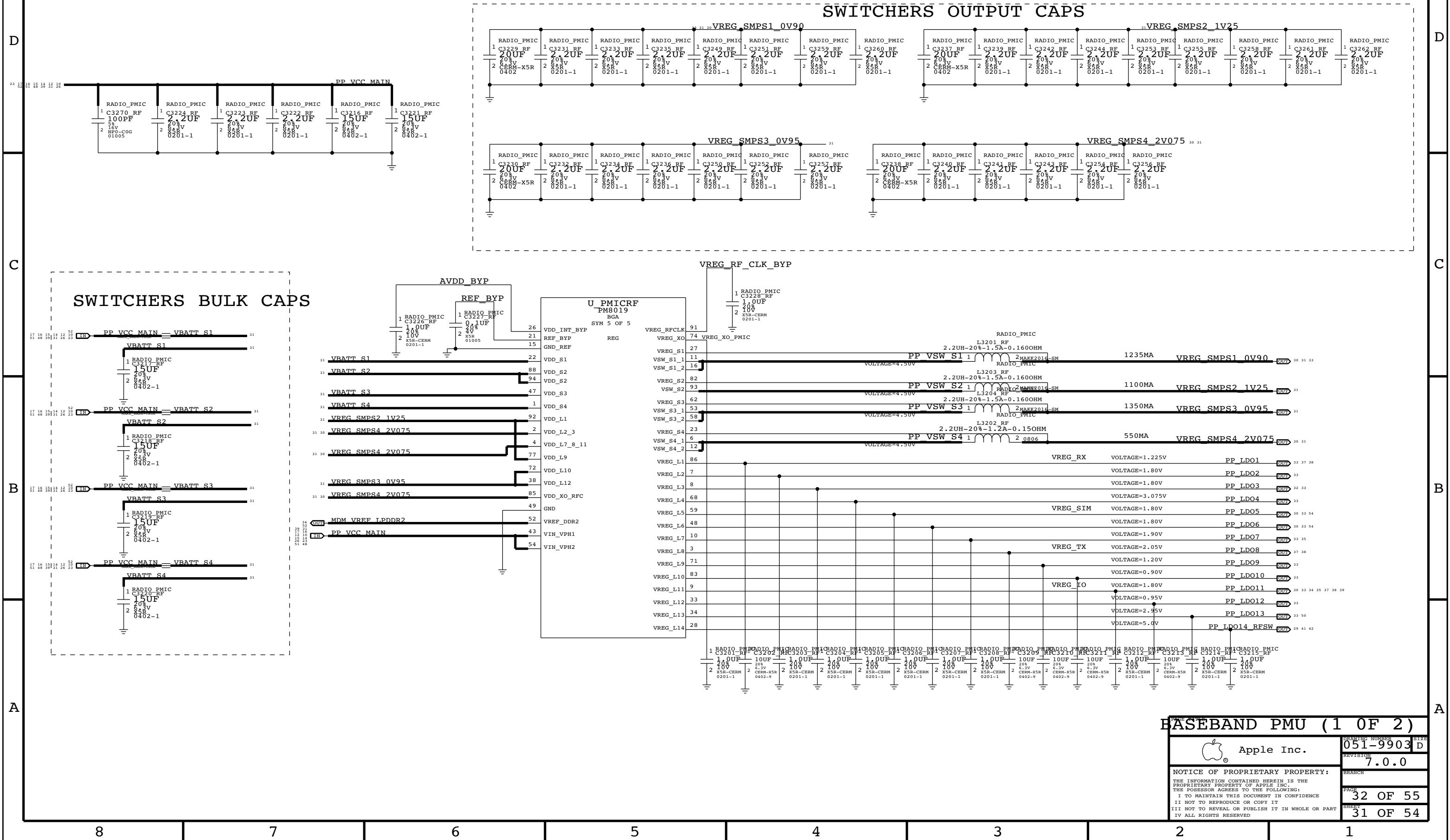


33 31	XW3101 RF	SHORT-10L-0.1MM-SM	ADC_SMPS1	29
38 37 35 34 33 31 30	PP_LDO11	SHORT-10L-0.1MM-SM	ADC_PP_LDO11	29
54 33 31 30	PP_LDO5	SHORT-10L-0.1MM-SM	ADC_PP_LDO5	29
31	VREG_SMPS4 2V075	SHORT-10L-0.1MM-SM	ADC_SMPS4	29

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BASEBAND PMU (1 OF 2)

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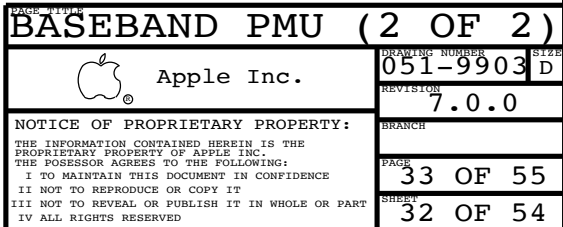


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BOARD	REVISION
0.00V	N61 PROTO MLB1
0.50V	N61 DEV3
0.70V	N61 DEV4
0.90V	N61 PROTO MLB2
1.10V	N61/N56 PROTO1
1.30V	N61/N56 PROTO2
1.40V	N61/N56 EVT1
1.50V	N61/N56 EVT2 (CARRIER)
1.60V	N61/N56 DVT
1.70V	N61/N56 PVT



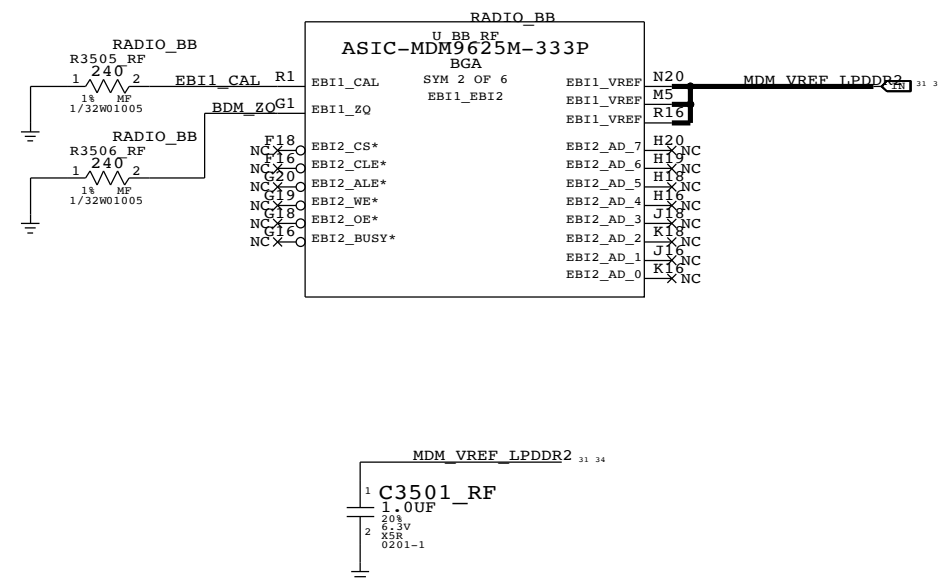
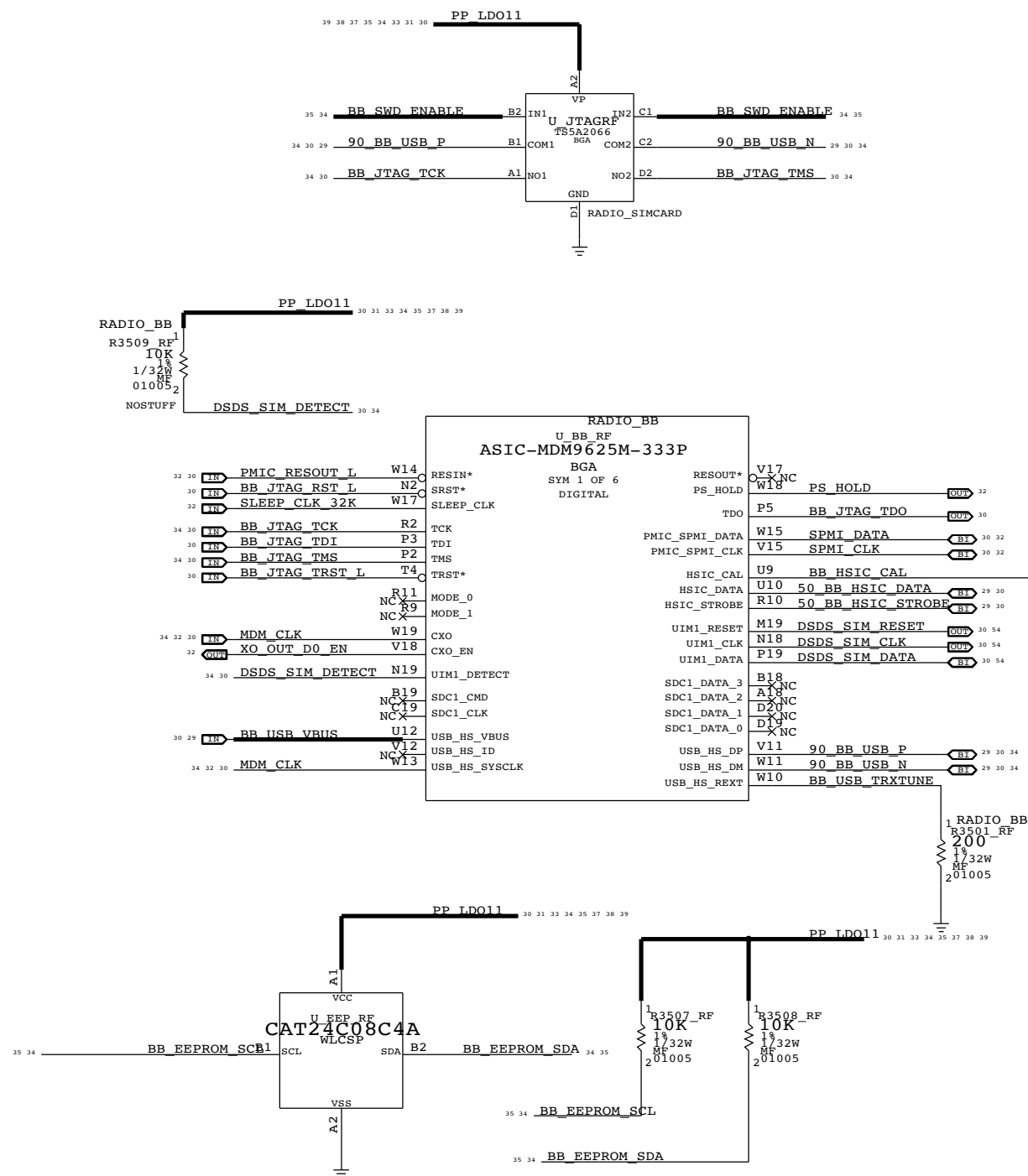
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U502



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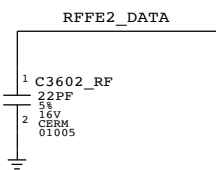
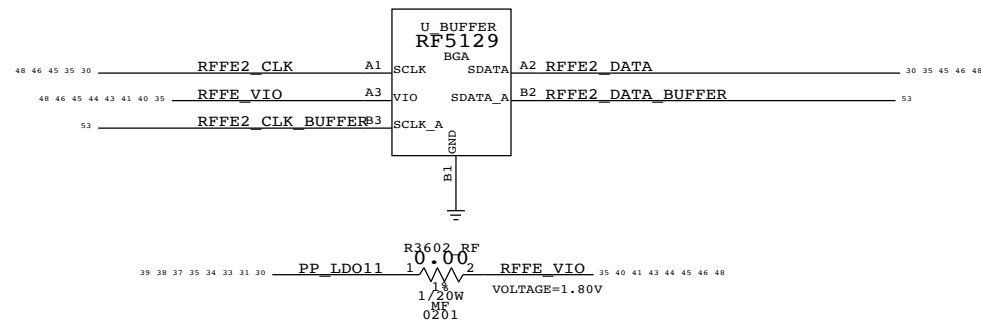
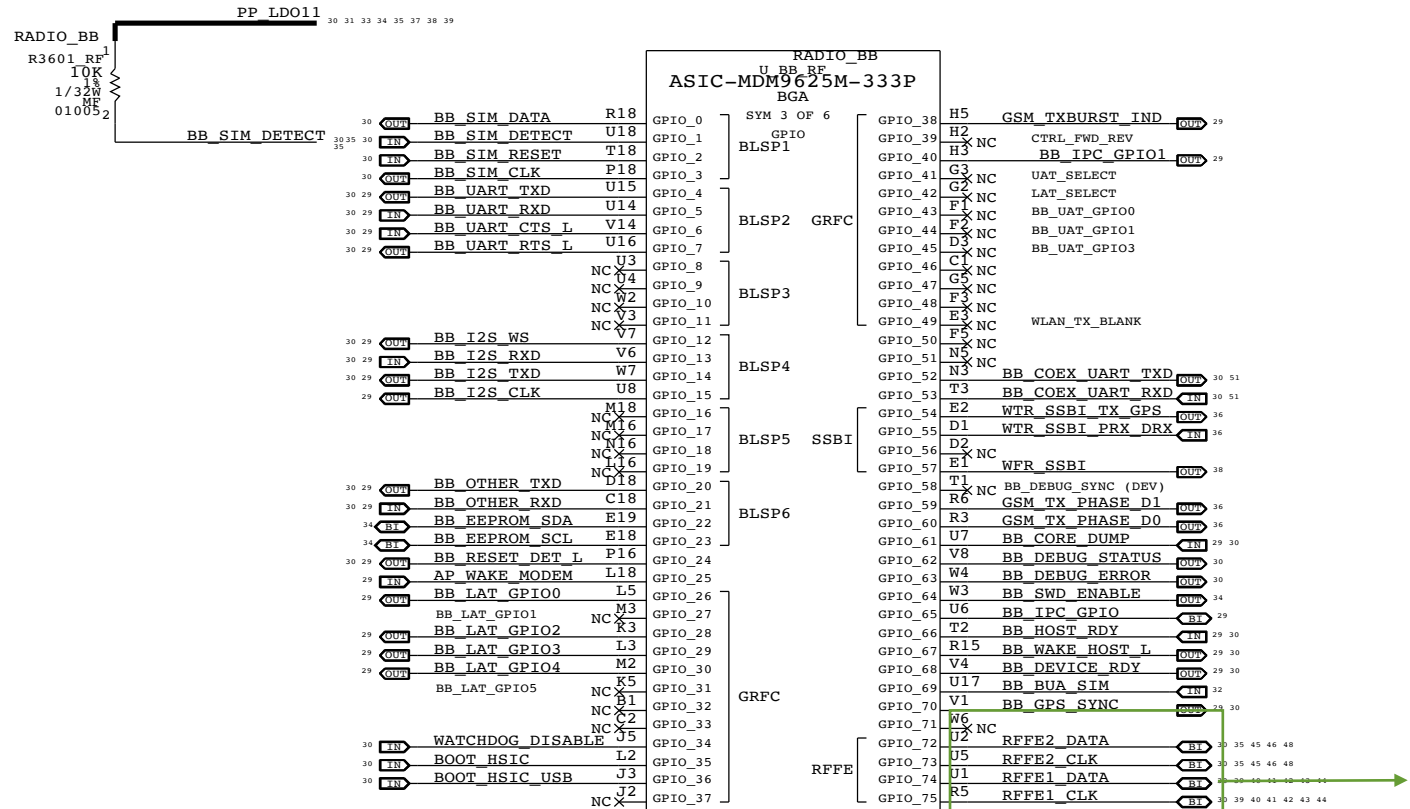
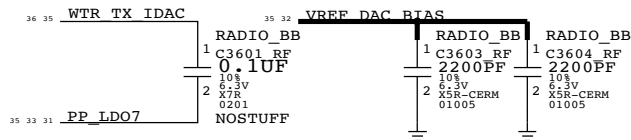
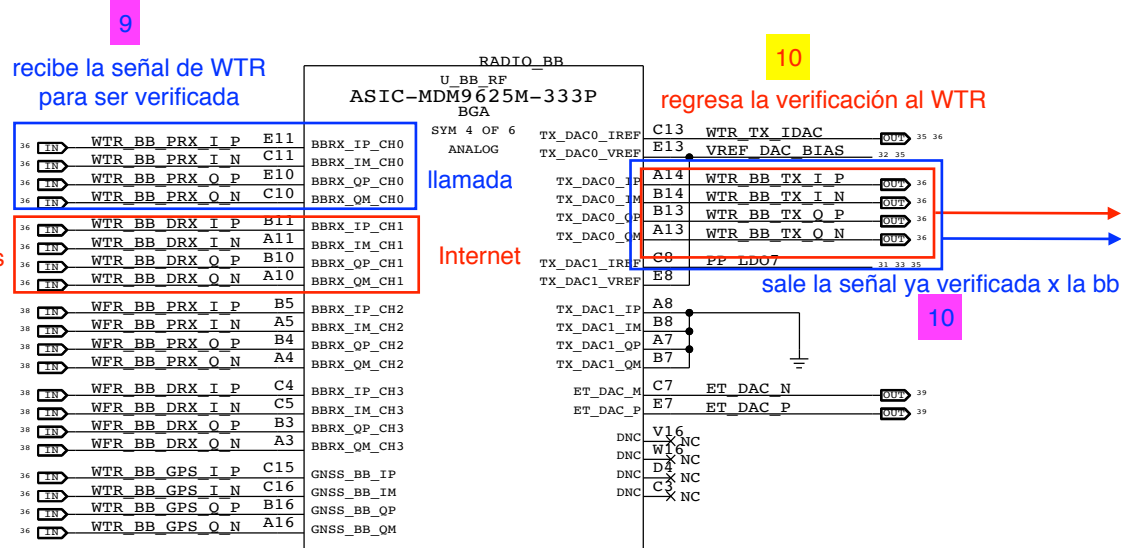
C600
R606
L600
U602



BASEBAND (3 OF 3)

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C704
R700
L700
U702



MOBILE DATA MODEM (2 OF 2)

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WTR TRANSCEIVER (1 OF 2)

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C802
R802
L800
U803

D

D

C

C

B

B

A

A

Entra las bandas al WTR

42	50_B8_PRX_WTR_IN	102	PRX_LB1_IN
42	50_B20_PRX_WTR_IN	92	PRX_LB2_IN
42	50_B26_PRX_WTR_IN	73	PRX_LB3_IN
41	50_B13_B17_B28_B29_PRX_WTR_IN	67	PRX_LB4_IN
38	50_WFR_PRX_LB_CA_IN	91	PRX_LB_CA_OUT
38	50_WFR_PRX_MB_CA_OUT	50	PRX_MB1_IN
45	50_B34_B39_PRX_WTR_IN	51	PRX_MB1_IN
47	50_DCS_WTR_IN	43	PRX_MB2_IN
47	50_PCS_WTR_IN	27	PRX_MB3_IN
46	50_B40A_PRX_WTR_IN	19	PRX_HMB4_IN
46	50_B40B_B38X_PRX_WTR_IN	9	PRX_HB1_IN
46	50_B41A_PRX_WTR_IN	17	PRX_HB2_IN
44	50_B7_PRX_WTR_IN	18	PRX_HB3_IN
		33	PRX_HB_CA_OUT

NCX

U_WTR_RF
WTR1625
BGA
SYM 1 OF 5

PRX_BB_IP	99	WTR_BB_PRX_I_P	35
PRX_BB_IM	108	WTR_BB_PRX_I_N	35
PRX_BB_QP	107	WTR_BB_PRX_Q_P	35
PRX_BB_QM	97	WTR_BB_PRX_Q_N	35

el WTR envia señal de verificación a la BB

LB1	DC
LB2	DC
LB3	DC
LB4	DC
MB1	NO DC
MB2	DC
MB3	DC
HB1	NO DC
HB2	DC
HB3	DC
HMB4	NO DC

47	50_B8_B28B_DRX_WTR_IN	5	DRX_LB1_IN
47	50_B13_B17_DRX_WTR_IN	15	DRX_LB2_IN
47	50_B26_B28A_DRX_WTR_IN	16	DRX_LB3_IN
47	50_B20_B29_DRX_WTR_IN	7	DRX_LB4_IN
38	50_WFR_DRX_LB_CA_IN	32	DRX_LB_CA_OUT
38	50_WFR_DRX_MB_CA_OUT	29	DRX_MB1_IN
47	50_B34_DRX_WTR_IN	28	DRX_MB1_IN
47	50_B39_DRX_WTR_IN	20	DRX_MB2_IN
		NCX	DRX_MB3_IN
47	50_B40_DRX_WTR_IN	2	DRX_HMB4_IN
47	50_B38X_DRX_WTR_IN	4	DRX_HB1_IN
47	50_B41A_DRX_WTR_IN	12	DRX_HB2_IN
47	50_B7_DRX_WTR_IN	13	DRX_HB3_IN
		NCX	DRX_HB_CA_OUT
45	100_GPS_WTR_IN_P	36	GNSS_RF_INP
45	100_GPS_WTR_IN_N	44	GNSS_RF_INM

WTR
procesa las señales

entrada de las frecuencias
para ser procesadas

la señal de entrada es verificada x la bb
el transeiver emite la señal a la bb

DRX_BB_IP	76	WTR_BB_DRX_I_P	35	RADIO_WTR
DRX_BB_IM	86	WTR_BB_DRX_I_N	35	RADIO_WTR
DRX_BB_QP	61	WTR_BB_DRX_Q_P	35	RADIO_WTR
DRX_BB_QM	68	WTR_BB_DRX_Q_N	35	RADIO_WTR
GNSS_BB_IP	60	WTR_BB_GPS_I_P	35	RADIO_WTR
GNSS_BB_IM	53	WTR_BB_GPS_I_N	35	RADIO_WTR
GNSS_BB_QP	67	WTR_BB_GPS_Q_P	35	RADIO_WTR
GNSS_BB_QM	85	WTR_BB_GPS_Q_N	35	RADIO_WTR

DNC

NC

el WTR recibe la verificación por parte de la bb

35	WTR_BB_TX_I_P	151
35	WTR_BB_TX_I_N	160
35	WTR_BB_TX_Q_P	152
35	WTR_BB_TX_Q_N	161
35	WTR_TX_IDAC	127

La señal es recibida,
ya verificada x la bb

35	GSM_TX_PHASE_D0	123	GP_DATA0
35	GSM_TX_PHASE_D1	104	GP_DATA1
		141	GND
		94	GND
		71	RTUNE
		140	GND
		55	GND
		118	GND
35	WTR_SSBI_TX_GPS	105	SSBI_TX_GNSS
35	WTR_SSBI_PRX_DRX	95	SSBI_PRX_DRX
		156	GND
		131	XO_IN

50_RF_CLK

1	C3702 RF
2	100PF
3	10V
4	COG
5	01005
6	NOSTUFF

RF_CLK IS SHARED BETWEEN WTR AND WFR. LENGTH DIFFERENCE BETWEEN THE TWO SHOULD BE < 5MM.

U_WTR_RF
WTR1625
BGA
SYM 3 OF 5

TX_BB_IP	151
TX_BB_IM	160
TX_BB_QP	152
TX_BB_QM	161
DAC_REF	127
GP_DATA0	123
GP_DATA1	104
GND	141
GND	94
RTUNE	71
GND	140
GND	55
GND	118
SSBI_TX_GNSS	105
SSBI_PRX_DRX	95
GND	156
XO_IN	131

TX_LB1_OUT	162	NC
TX_LB2_OUT	153	NC
TX_LB3_OUT	163	50_LB_2G_WTR_TX_OUT
TX_LB4_OUT	154	50_B8_B26_B20_WTR_TX_OUT
TX_MB1_OUT	146	50_B13_B17_B28_WTR_TX_OUT
TX_MB2_OUT	138	50_B3_B4_WTR_TX_OUT
TX_MB3_OUT	139	50_HB_2G_WTR_TX_OUT
TX_MB4_OUT	155	50_B1_B25_B34_B39_WTR_TX_OUT
TX_HB1_OUT	130	NC
TX_HB2_OUT	121	50_B7_WTR_TX_OUT
	122	50_B40_B38_B41_WTR_TX_OUT
ADC_IN	109	
PDET_RFFB	117	50_FWD_OR_REV_RF
GND	122	

salida del WTR

emisión

RF TRANSCEIVER (1 OF 3)

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	REVISION	7.0.0		
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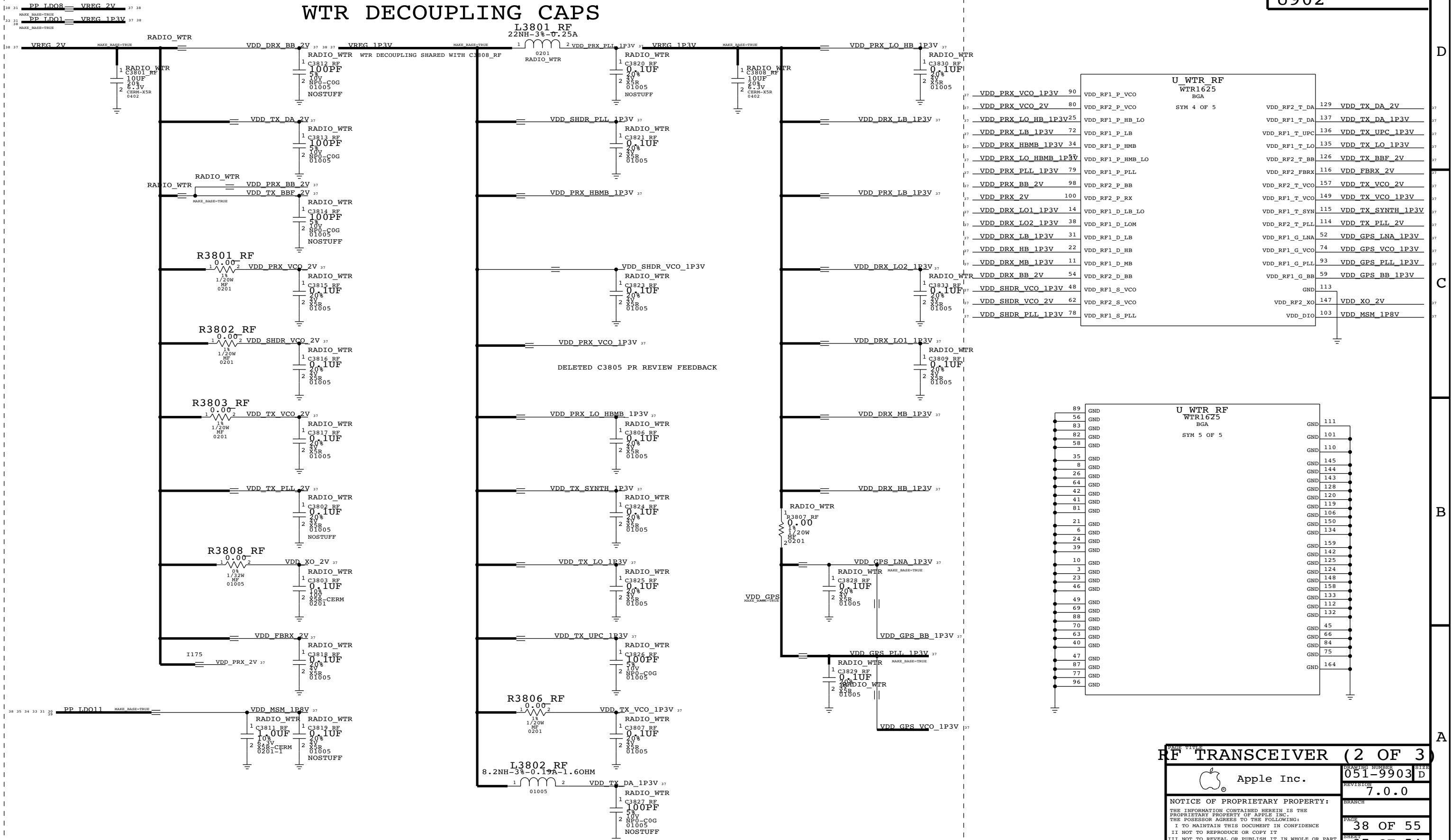
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C934	
R926	
L3802	RF
U902	

WTR DECOUPLING CAPS

L3801 RF
22NH-3%-0.25A



RF TRANSCEIVER (2 OF 3)



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051-9903		D
REVISION		
7.0.0		
BRANCH		
PAGE		
38 OF 55		
SHEET		
37 OF 54		

WFR TRANSCEIVER

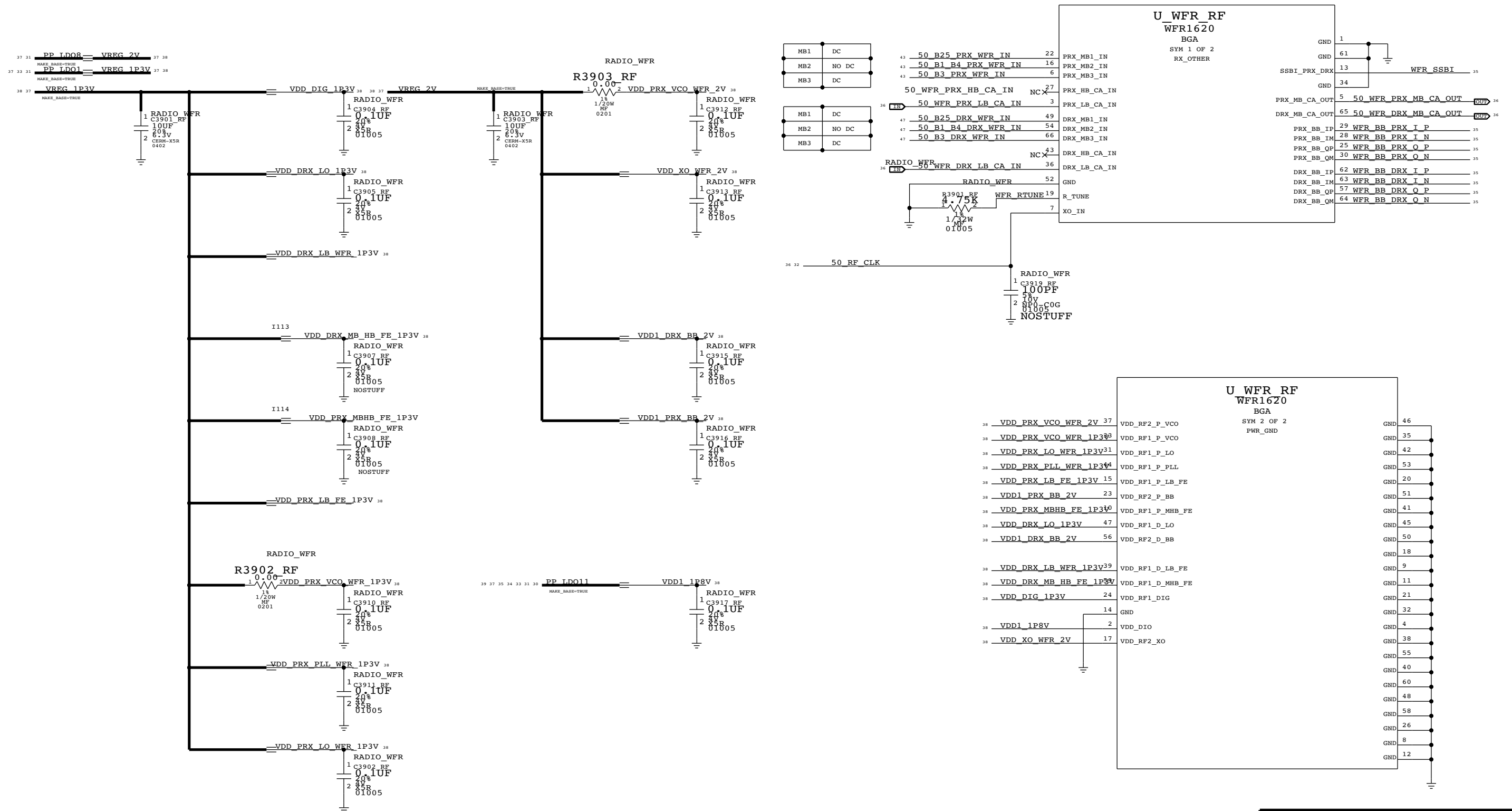
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C1019

R1016

L1000

U1002



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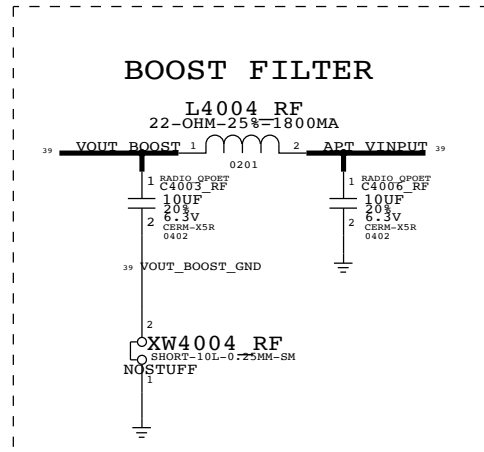
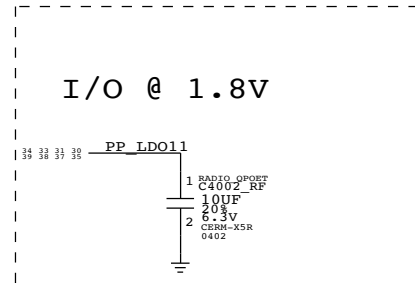
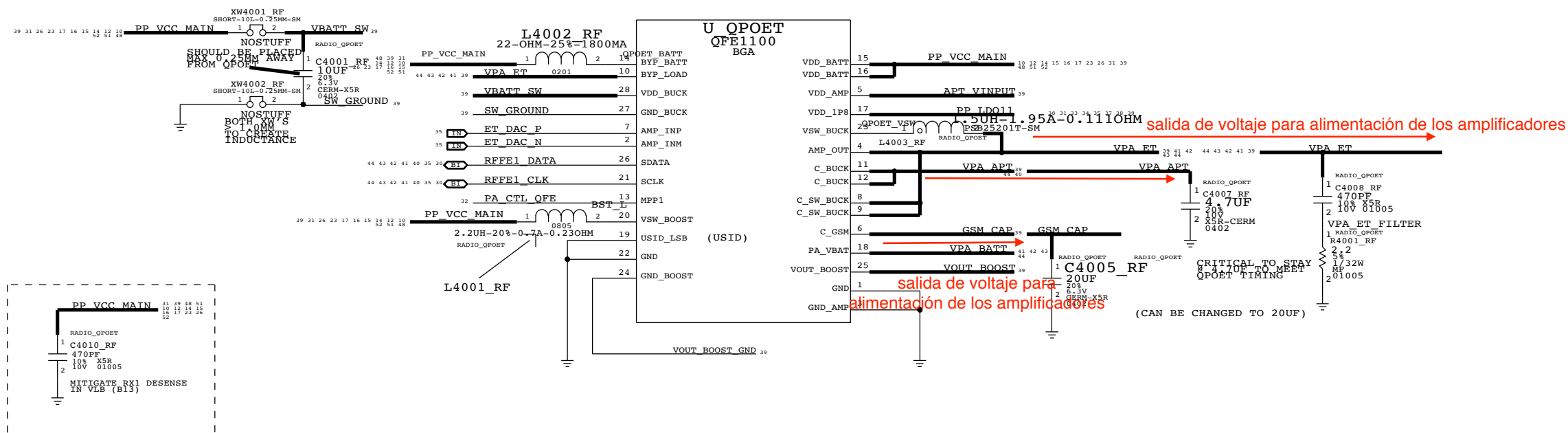
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39 OF 55

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C1110
R1102
L1104
U1101

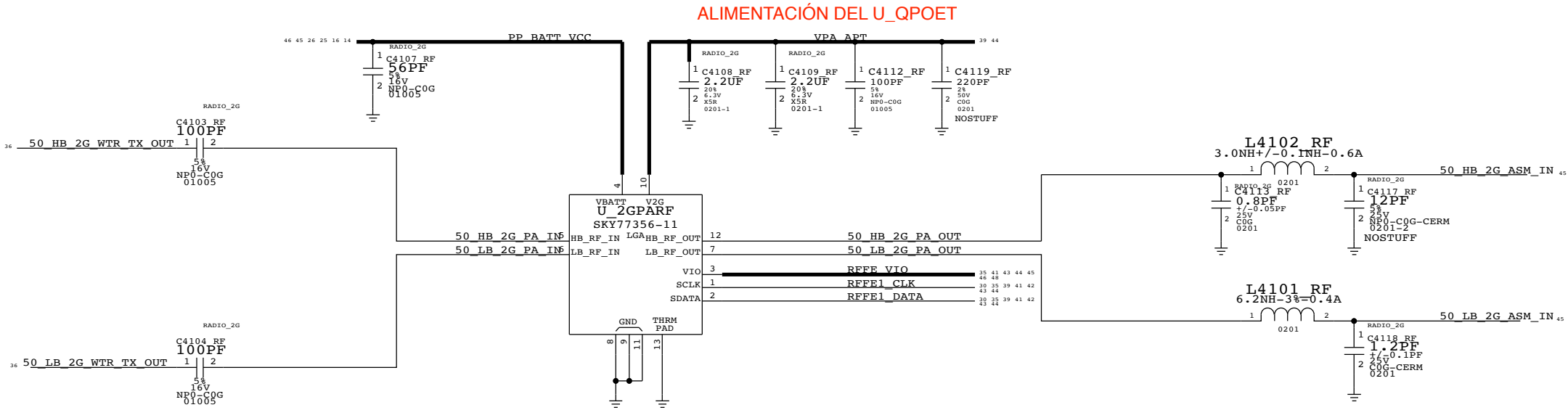


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2G PA

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C1208
R1200
L1204
U1201



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2G PA		
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	SHEET	40 OF 54

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C1332

R1300

L4215 RF

U1304



ALIMENTACIÓN DEL U_QPOET

ALIMENTACIÓN DEL U QPOET

U VLBPAD
KY77802-12
LGA

ADOR DE POTENCIA
ANDA BAJA

3G

El amplificador, emite las señales B13,B17,B28,B29

14 entran 3 bandas al divisor para ser separadas

salen las 3 bandas separadas

tira a tierra ciertas frecuencias

V2	V1	BAND
0	1	B28
1	0	B13
1	1	B17

SALE DEL ASM Y LLEGA AL AMPLIFICADOR 3G

por esta linea entrega
al ASM

11

VERY LOW BAND PAD

Apple Inc.

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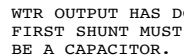
BRANCH

PAGE 42 OF 55

41 OF 54

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U1402



V2	V1	BAND
0	1	B8
1	0	B20
1	1	B26

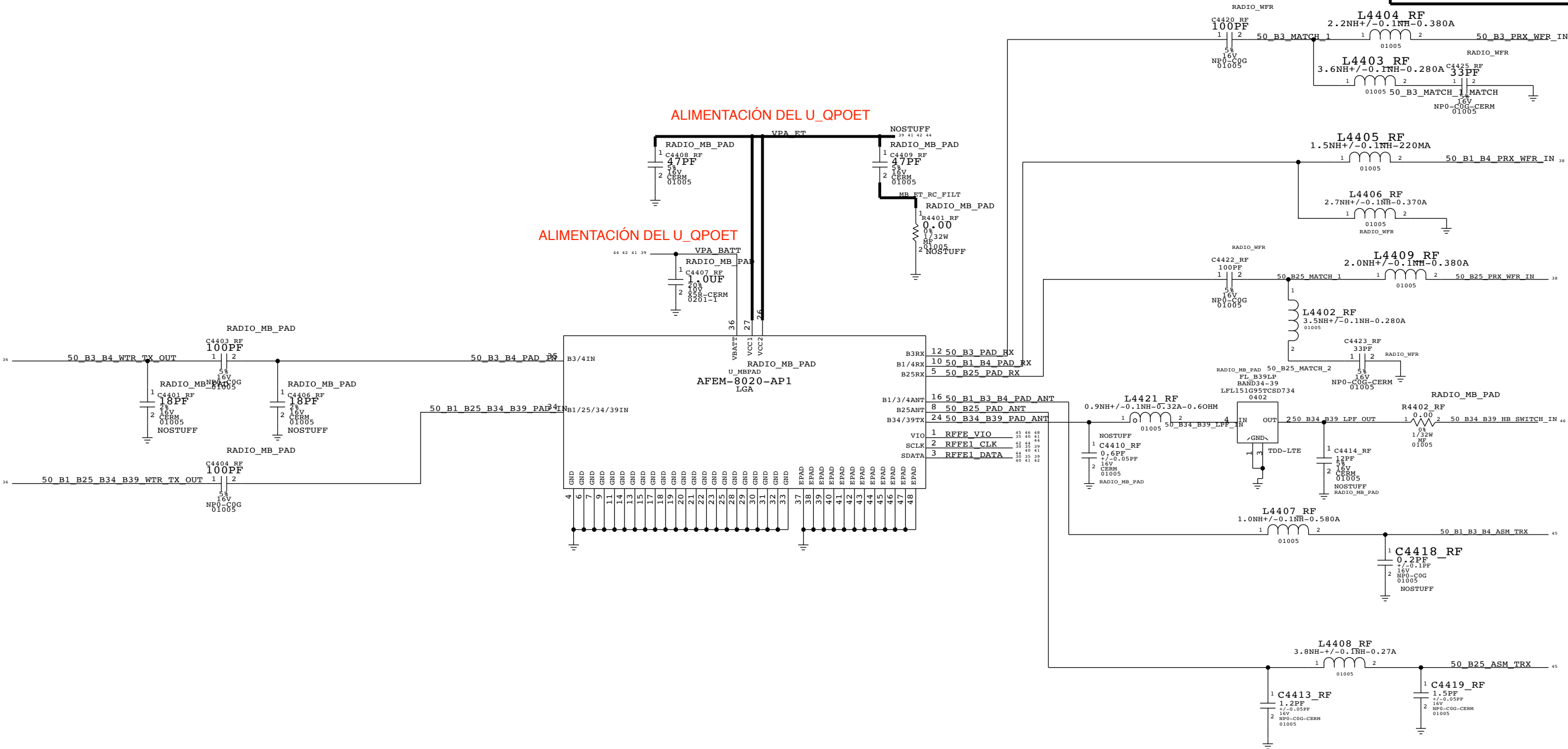
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MID BAND PAD (B1, B25, B3, B4, B34, B39)

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C4426 RF
R1500
L4409 RF
U1501

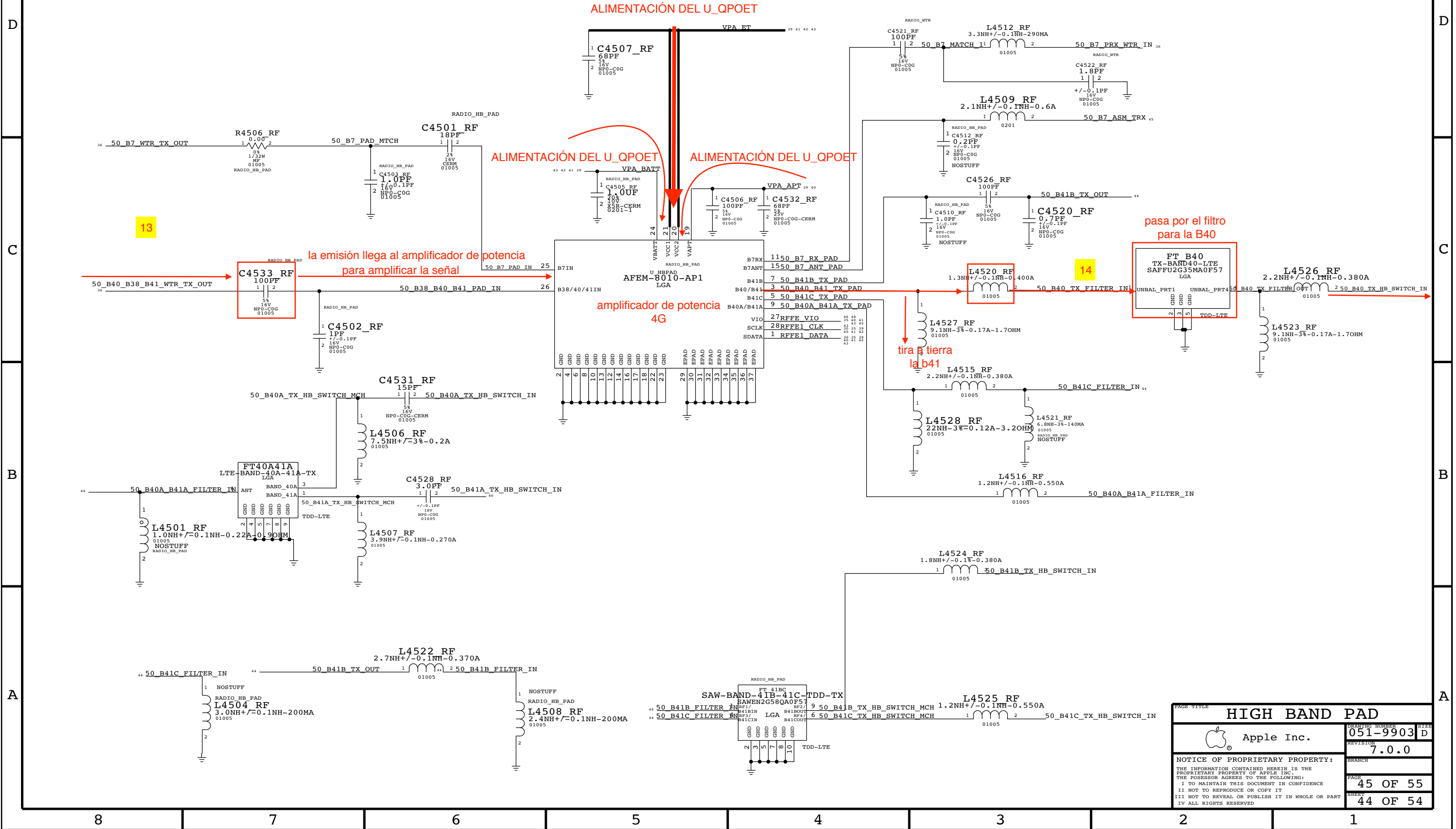


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MID BAND PAD		
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	PAGE	44 OF 55
	SHEET	43 OF 54

HIGH BAND PAD (B7, B38, B40, B41, XGP)

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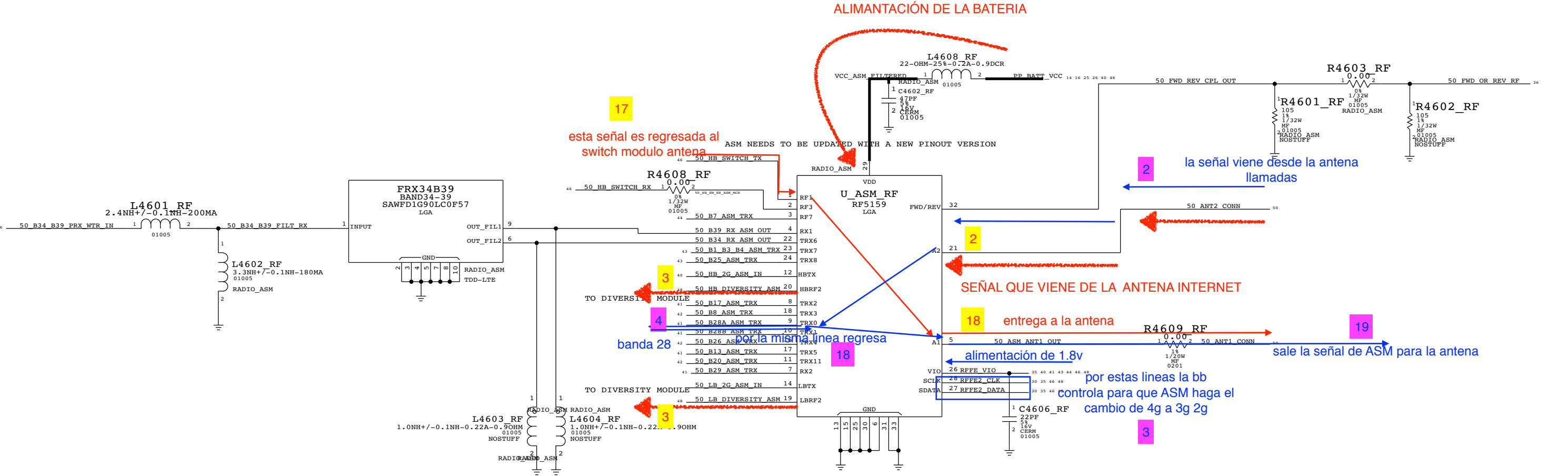
C4533	RF
R1600	
L1616	
U1601	



ANTENNA SWITCH

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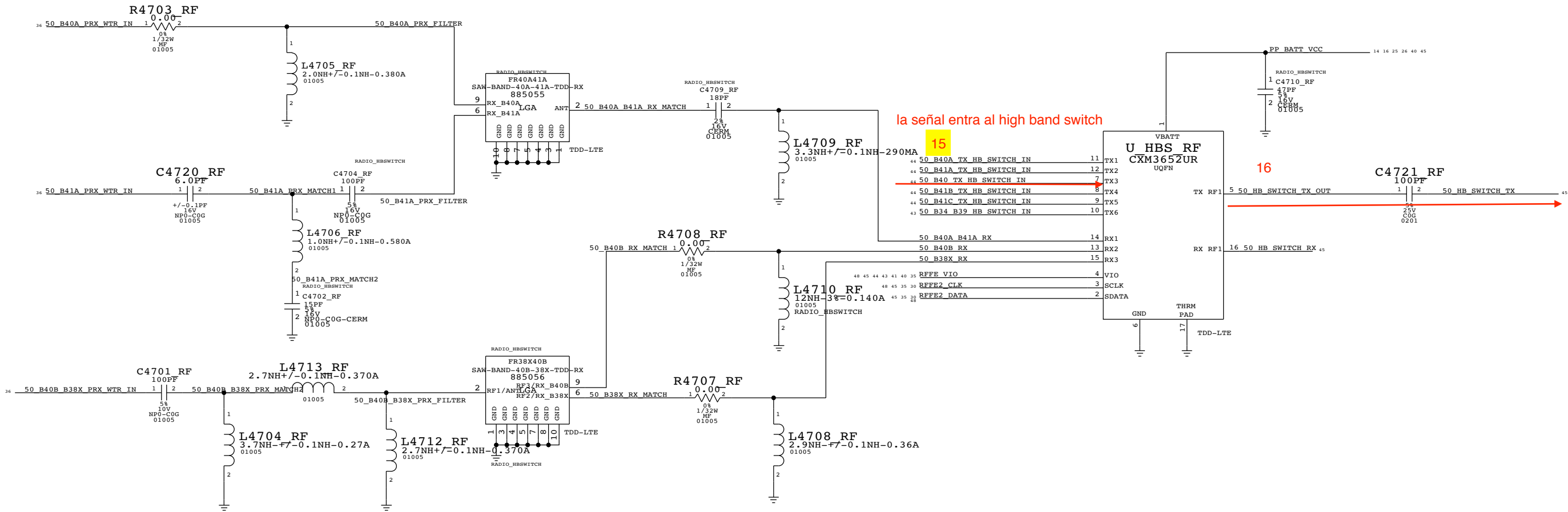
C1702
R1700
L4608 RF
U1702



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	PAGE	46 OF 55
	SHEET	45 OF 54

HIGH BAND SWITCH

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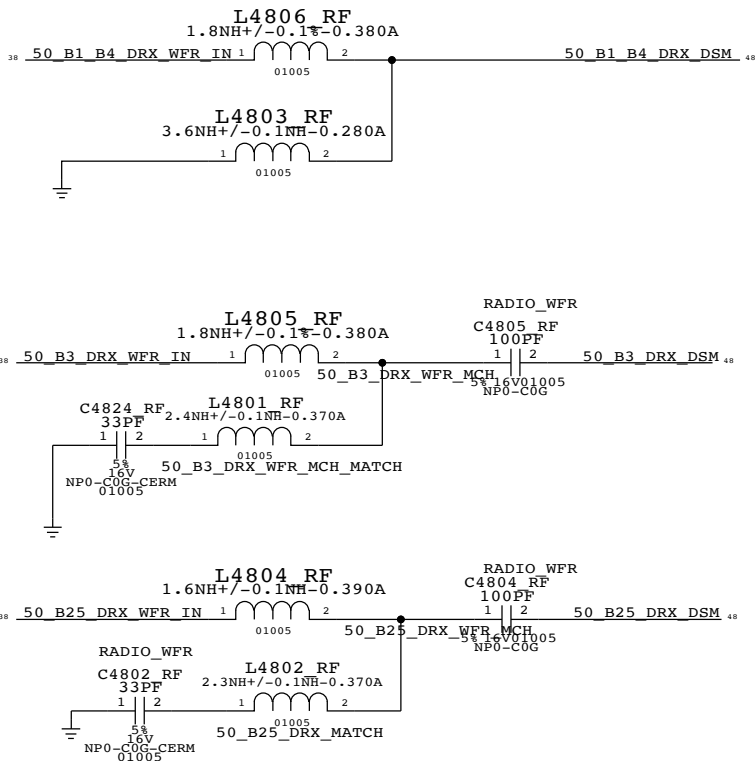
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	PAGE	47 OF 55
	SHEET	46 OF 54

RX DIVERSITY (1)

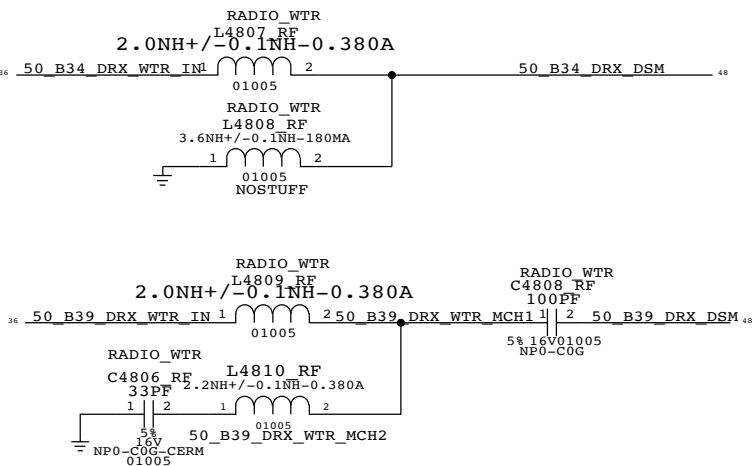
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C4826 RF
R1800
L1829
U1801

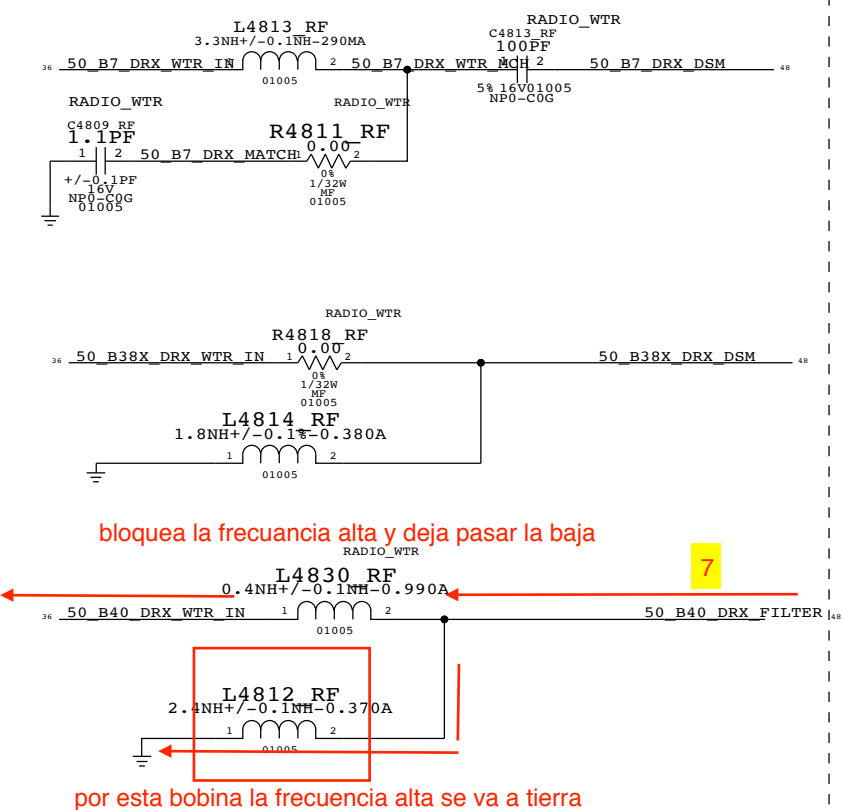
MIDBAND MIDBAND DIVERSITY - WFR



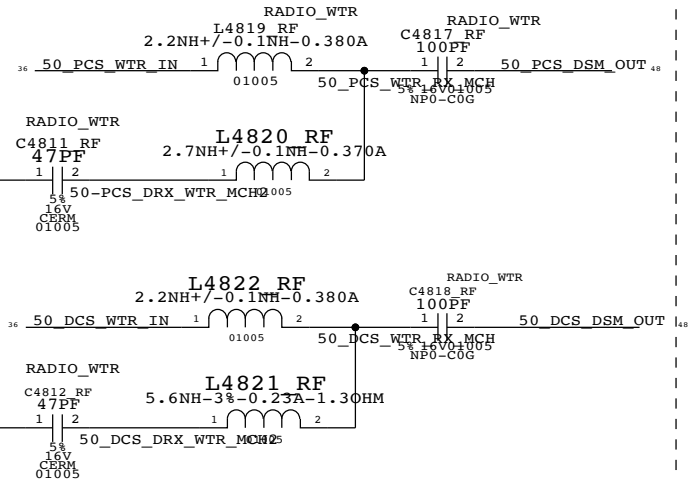
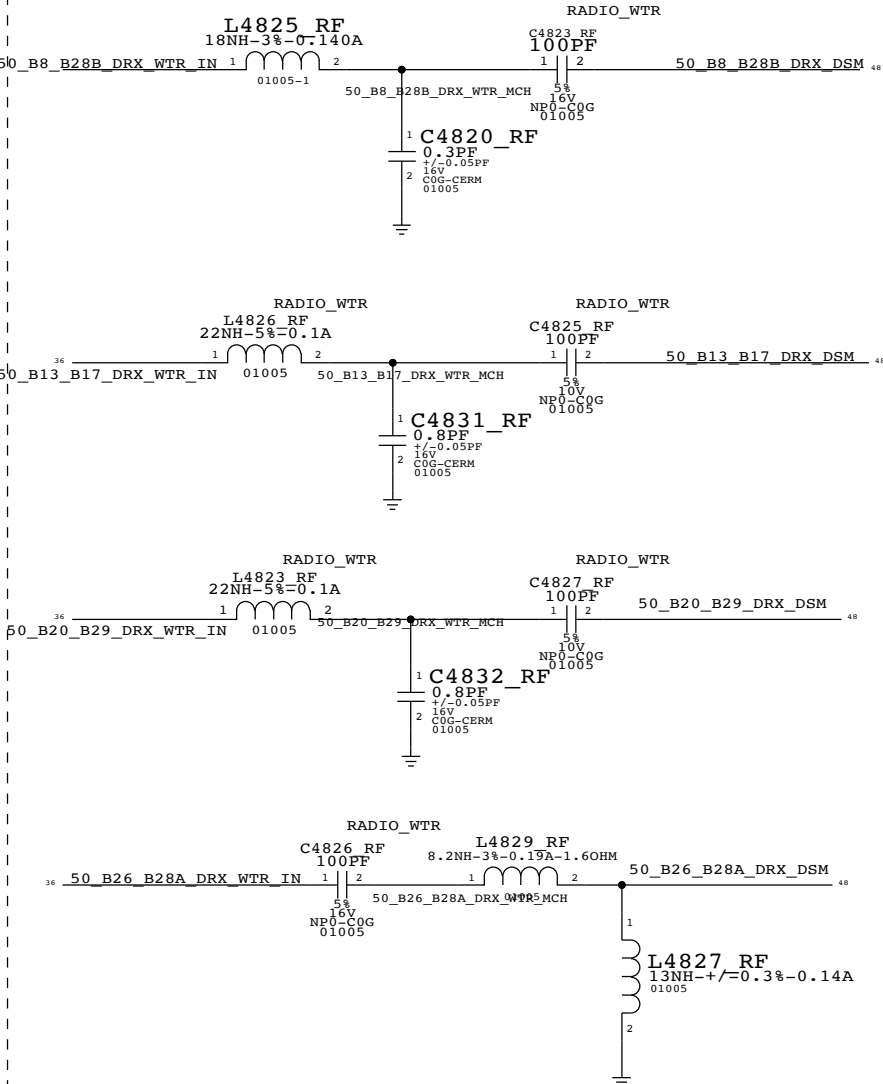
MIDBAND DIVERSITY - WTR



HIGHBAND DIVERSITY - WTR



LOWBAND DIVERSITY - WTR



RX DIVERSITY

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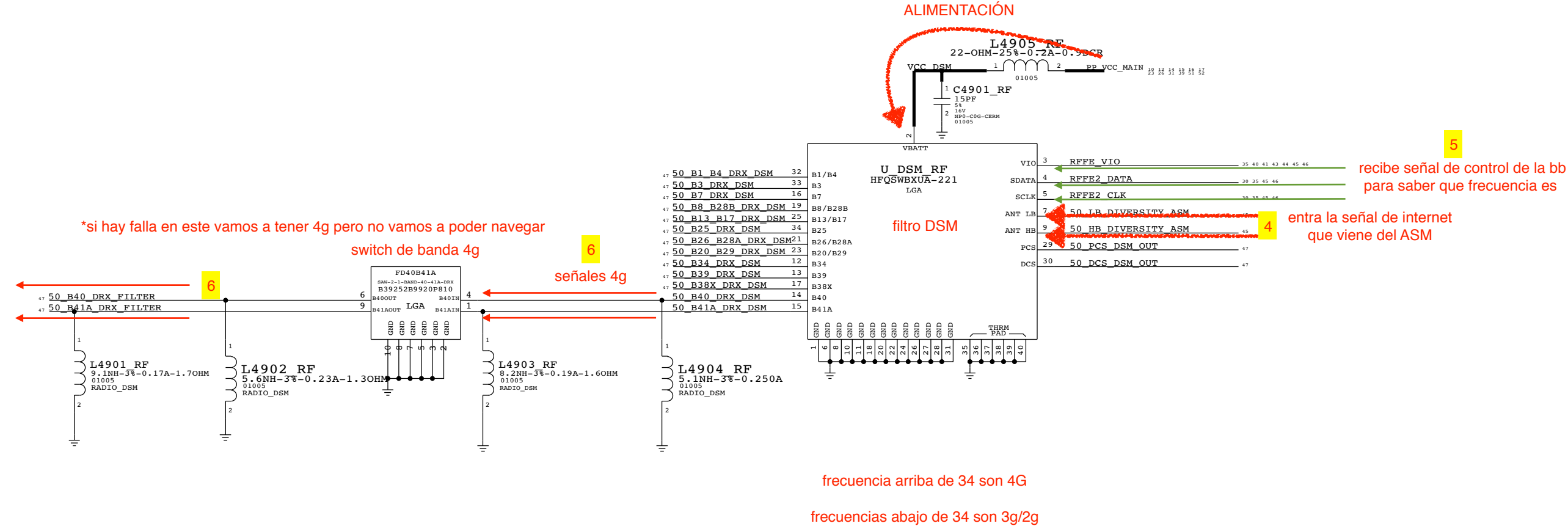
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PAGE
48 OF 55
SHEET
47 OF 54

RX DIVERSITY (2)

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C1900
R1900
L1900
U1901

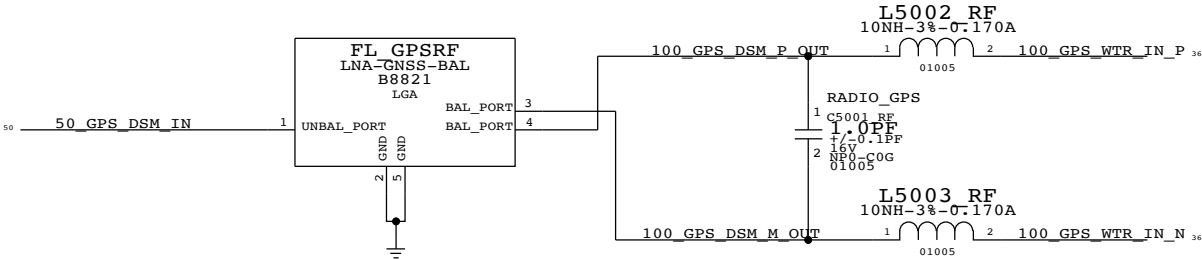


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GPS

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C1900
R1900
L1900
U1901



GPS		
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	SHEET	49 OF 54

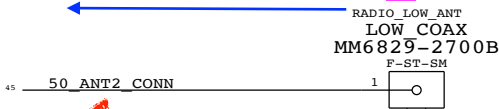
ANTENNA FEED'S

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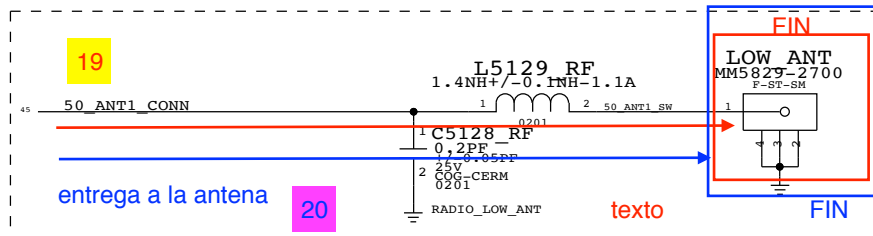
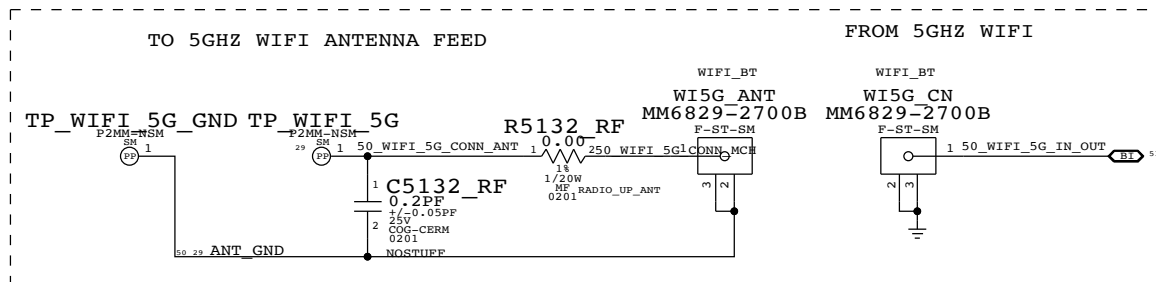
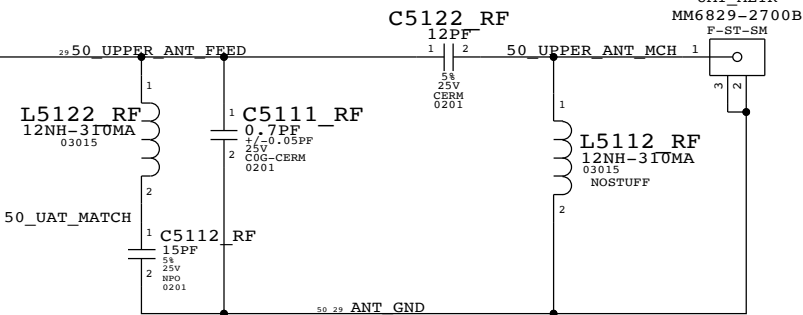
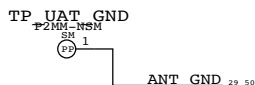
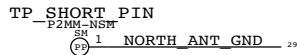
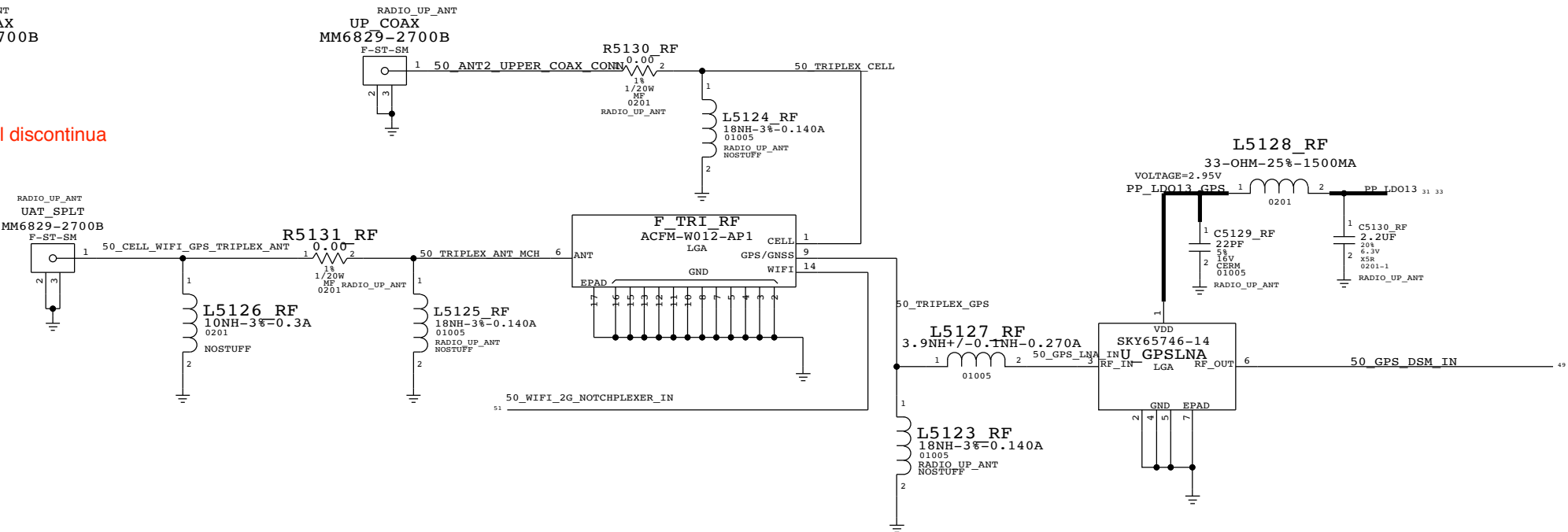
TEST & COAX CONNECTOR FOR LOWER SECTION OF MLB

de aqui inicia la señal para llamadas

1



desde esta antena entra la señal para el DRX señal discontinua al ASM intenet 4G



PAGE TITLE		ANTENNA FEEDS		
 Apple Inc.	DRAWING NUMBER	051-9903	SIZE D	
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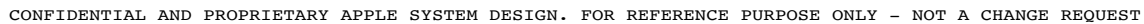
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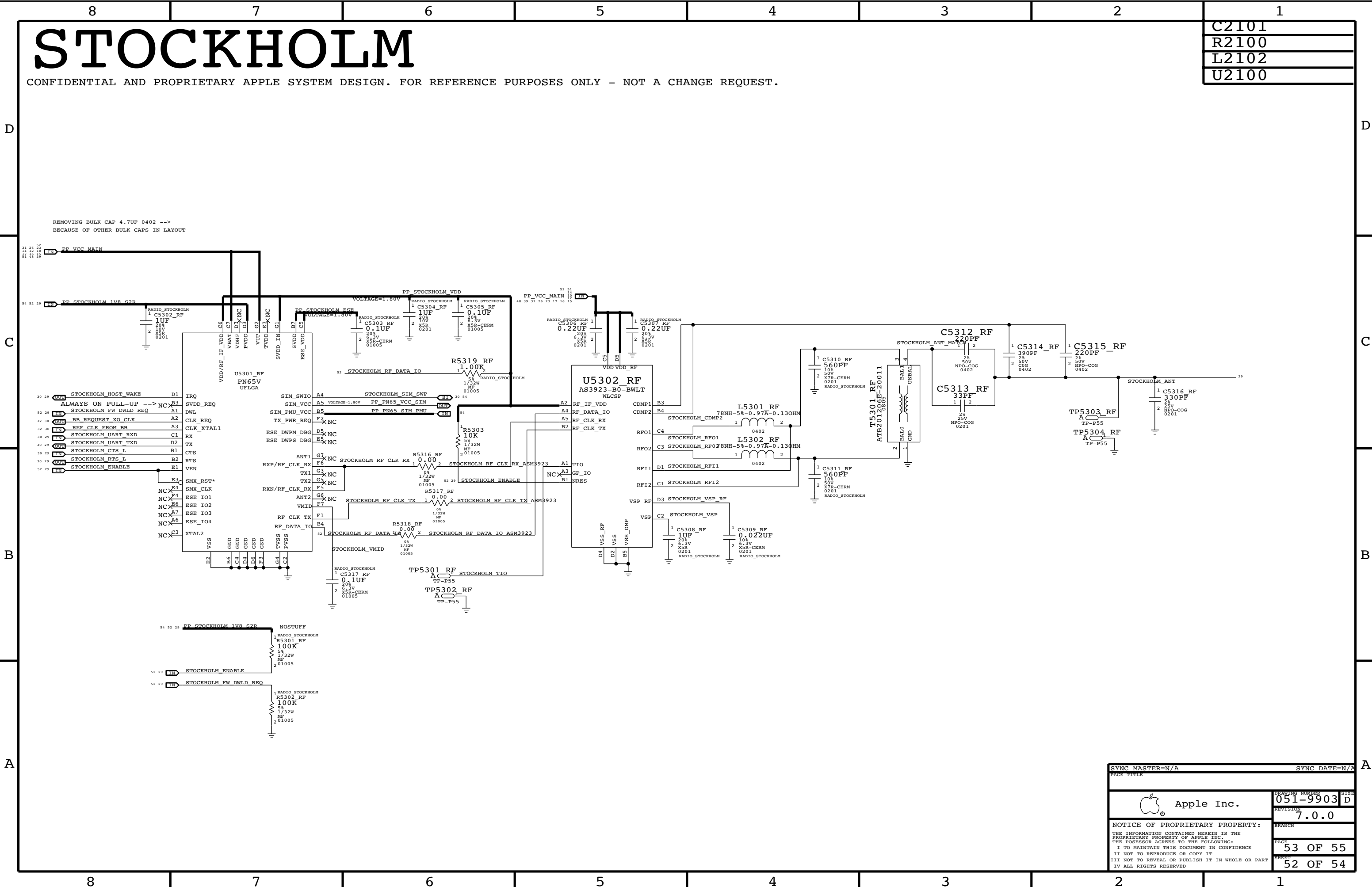


B



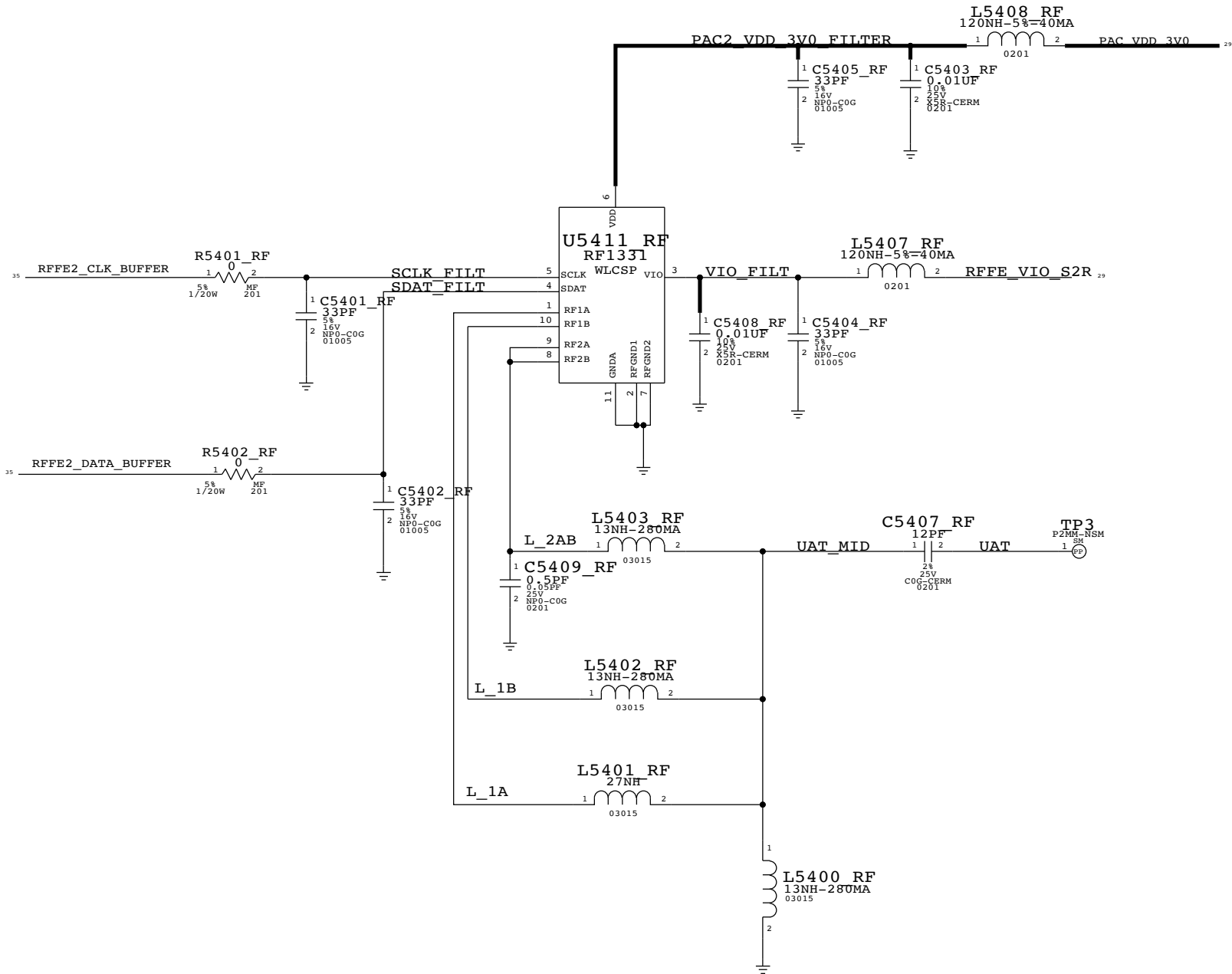
MODULE BOOT-STRAPPED TO PCIE INTERNALLY

A

[illegible][illegible][illegible][illegible]

ON-BOARD JUMPER FLEX

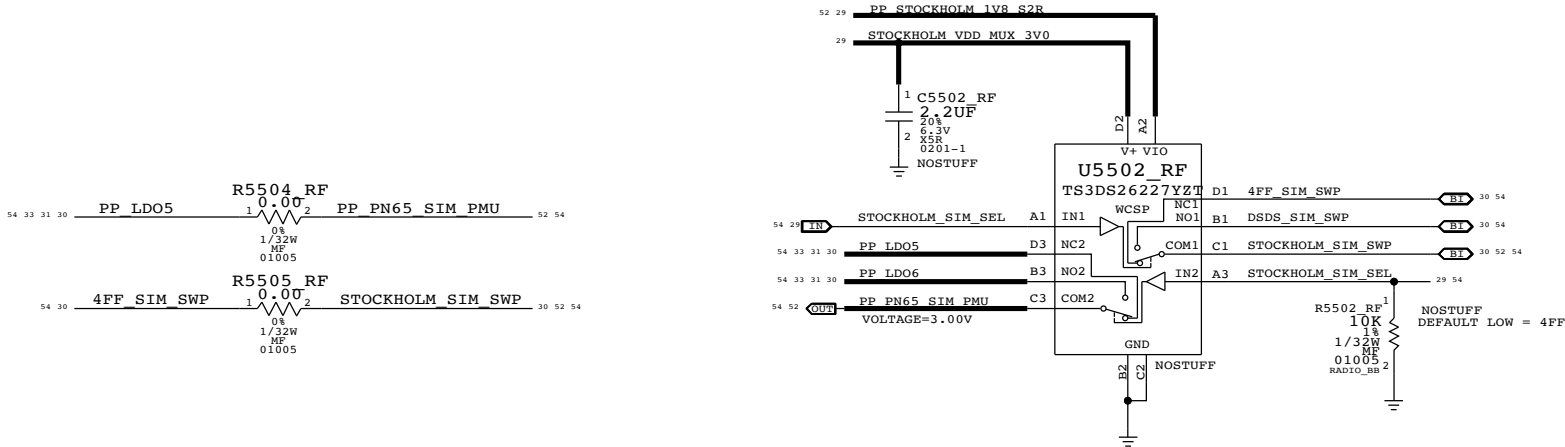
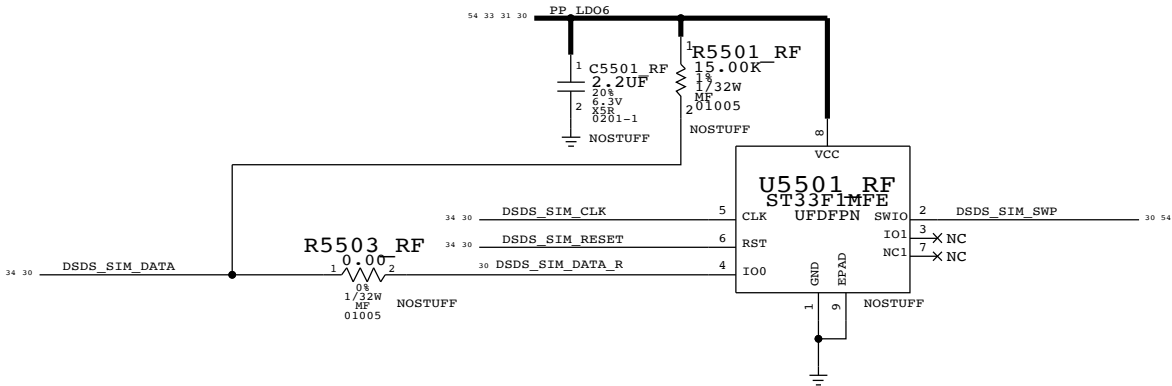
UAT JUMPER



PAGE TITLE		
JUMPER		
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DSDS

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